Chapter 4. Single Electron Transistors

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4.1 Project Description

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Two years ago we discovered, completely unexpectedly, new behavior in very small Si MOSFETs. Whereas conventional transistors turn from the off state to the on state only once as the gate voltage increases, these small transistors turn on and off periodically, in some cases as many as one hundred times. We have since learned how to control the period of these oscillations in GaAs field effect devices and have shown that a nm-size transistor turns on and off once for every electron added to it. Because of this, we call this kind of device a Single Electron Transistor. Having established that each cycle corresponds to the addition of one electron, the period in gate voltage provides a very precise measure of the capacitance of our devices. Because it is very small, \( \sim 10^{-16} \) F, such transistors may eventually find application in ultra-sensitive charge detection. Different applications utilizing the multistate characteristics of the devices can be imagined. Unfortunately, so far the single electron behavior has only been observed below about 1 K. Whether this behavior can be observed at higher temperatures in the future depends on the details of the mechanism that causes it. Our objective is, therefore, to better understand that mechanism.

Our initial discovery was made3 with inversion layers in Si which were about 30 nm wide and several mm long. These were dual gate MOSFETs with a narrow slot, \( \sim 600 \) nm wide, in the lower gate, fashioned with x-ray lithography. This lower gate was made of refractory metals allowing a high temperature anneal after all unconventional lithography was complete. In the end, these devices were the narrowest transistors ever made, but while most previous narrow devices had low mobility, these had mobilities comparable to wide devices. The successful fabrication of these ultra-narrow devices was the result of a close collaboration between H.I. Smith, D.A. Antoniadis, M.A. Kastner and their students.

Soon after these devices were first fabricated, it was discovered that their conductance oscillated periodically as a function of the density of electrons in the conducting channel, proportional to the gate voltage, \( V_g \). An example of this distinctive behavior is shown in figure 1.

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The foremost problem posed by our discovery was to find out what determined the period of the conductance oscillations. We soon discovered that the period varied at random from device to device, indicating that it was determined by the random spacing of interface charges that happened to fall over the conducting channel. This suggested that potential barriers resulting from the interface charges were isolating a segment of the channel and that the conductance underwent one cycle every time the same number of electrons was added to the isolated segment. To prove this, and to ascertain what that number of electrons was, new devices had to be made.

The first step was to produce devices close in their operation to a MOSFET but in GaAs where the influence of interface charges is much weaker.\(^4\) This was accomplished through a remarkably successful collaboration with M. Heiblum of IBM. The new structure was created by growing AlGaAs on a conductive substrate, followed by a layer of undoped GaAs. The electron density at the GaAs/AlGaAs interface, inverted from the usual configuration was varied by applying a voltage to the substrate. Heiblum was able to grow such inverted heterojunctions with mobilities of \(\sim 500,000 \text{ cm}^2/\text{V} \cdot \text{s}\).

Next, the conducting channel is defined by depositing a metal gate on top of the GaAs, and a gap is patterned in the gate by electron beam lithography. Because a depletion region is created under the gate, the electrons accumulate only under the gap. In order to emulate the effect of two charged impurities, two constrictions are patterned in the gap, as illustrated in figure 2. The electron beam lithography was done at IBM with the help of S. Wind.

Devices of the kind depicted in figure 2 worked exactly as we hoped they would.\(^5\) Periodic oscillations of the conductance were seen again, but now they were controlled: The period was the same for different devices with the same spacing between constrictions and was larger when the spacing was shorter, which is consistent with the idea that the same number of electrons is added for one period in all devices. This is illustrated in figure 3. Calculations of the capacitance of such devices are only consistent with each period corresponding to one electron added per oscillation.

This is an amazing result: Figure 3 shows that for the shortest structures the conductance consists of periodic, narrow, well-separated resonances. The conductance rises and falls by a factor \(\sim 100\) with a variation of gate voltage that corresponds to only \(\sim 1/10\) of an electron.


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We are not yet sure what causes the Single Electron Transistor to work the way it does. The conductance appears to result from resonant tunneling, but the nature of the states that are resonant is still unclear. Patrick Lee and his collaborators believe that the states arise from single particle quantum states (the Fabry-Perot states induced by the two barriers, the same as expected for any "quantum dot"), but that the energies of the levels are dominated by the Coulomb interaction between the electrons in the isolated segment. Van Houten and Beenakker argued that the Coulomb interaction alone could explain the phenomena we see, and that the effect was the same as the Coulomb blockade recently popularized by Likharev. Some of our measurements suggest, however, that electron-electron correlation, in addition to the simple Coulomb interaction, may be important.

Figure 2.
Figure 3.