2.1 Project Description

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Several years ago, we discovered, completely unexpectedly, new behavior in very small Si MOSFETs. Whereas conventional transistors turn from the off state to the on state only once as the gate voltage increases, these small transistors turn on and off periodically, in some cases as many as one hundred times. We have since learned how to control the period of these oscillations in GaAs field effect devices and have shown that a nm-size transistor turns on and off once for every electron added to it. Because of this, we call such a device a Single Electron Transistor.

Having established that each cycle corresponds to the addition of one electron, the period in gate voltage provides a very precise measure of the capacitance of our devices. Because it is very small, \( \sim 10^{-16} \text{ F} \), such transistors may eventually find application in ultra-sensitive charge detection. Furthermore, capacitive coupling may make possible arrays of single-electron transistors with unique properties. Unfortunately, the single electron behavior has only been observed, so far, below about 1 K. Whether this behavior can be observed at higher temperatures in the future depends on the details of the mechanism that causes it. Our objective is, therefore, to better understand that mechanism.

Our initial discovery was made with inversion layers in Si which were about 30 nm wide and several mm long. These were dual gate MOSFETs with a narrow slot, \( \sim 600 \text{ nm} \) wide, in the lower gate, fashioned with X-ray lithography. This lower gate was made of refractory metals allowing a high temperature anneal after all unconventional lithography was complete. In the end, these devices were the narrowest transistors ever made, but whereas most previous narrow devices had low mobility, these had mobilities comparable to wide devices. The successful fabrication of these ultra-narrow devices was the result of a close collaboration between Professor Henry I. Smith, Professor Dimitri A. Antoniadis, and Professor Marc A. Kastner and their students.

Soon after these devices were first fabricated, it was discovered that their conductance oscillated periodically as a function of the density of electrons in the conducting channel, proportional to the gate voltage, \( V_g \). To prove that the period of the oscillations corresponded to the addition of a single electron, new devices had to be made. The first step was to produce devices close in their operation to a MOSFET but in GaAs where the influence of interface charges is much weaker. This was accomplished through a remarkably suc-

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cessful collaboration with M. Heiblum of IBM. The new structure was created by growing AlGaAs on a conductive substrate, followed by a layer of undoped GaAs. The electron density at the GaAs/AlGaAs interface, inverted from the usual configuration, was varied by applying a voltage to the substrate. Heiblum was able to grow such inverted heterojunctions with mobilities of \( \sim 500,000 \text{cm}^2/\text{V-s} \).

Next, the conducting channel is defined by depositing a metal gate on top of the GaAs, and a gap is patterned in the gate by electron beam lithography. Because a depletion region is created under the gate, the electrons accumulate only under the gap. In order to emulate the effect of two charged impurities, two constrictions are patterned in the gap. The electron beam lithography was done at IBM with the help of Shalom Wind.

Devices of this kind worked exactly as we hoped they would.\(^4\) Periodic oscillations of the conductance were seen again, but now they were controlled: The period was the same for different devices with the same spacing between constrictions and was larger when the spacing was shorter, consistent with the idea that the same number of electrons is added for one period in all devices. Calculations of the capacitance of such devices are only consistent with each period corresponding to one electron added per oscillation.

This is an amazing result: Figure 1 shows that the conductance consists of periodic, narrow, well-separated resonances. The conductance rises and falls by a factor \( \sim 1000 \) with a variation of gate voltage that corresponds to only \( \sim 1/3 \) of an electron.

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We now understand quite well why the Single Electron Transistor works the way it does. The conductance results from resonant tunneling, and Professor Patrick Lee and his collaborators have shown that the states arise from single particle quantum states (the Fabry-Perot states induced by the two barriers, the same as expected for any “quantum dot”), but that the energies of the levels are dominated by the Coulomb interaction between the electrons in the isolated segment.

Experiments in the quantum Hall regime at first seemed to indicate that the nature of the quantum states was very simple. By measuring the position in gate voltage of a series of conductance resonances, we were able to measure the energies for adding a series of electrons as a function of magnetic field. Figure 2 shows (top panel) how the peak positions oscillated with magnetic field. Subtracting a constant $V_g$ difference from each pair of curves we emulated the effect of subtracting a constant Coulomb energy. We thereby obtained the level spectrum shown in the lower panel.

The level spectrum looks remarkably similar to that for non-interacting electrons in a magnetic field and a confining electrostatic potential. We thought, therefore, that the simple Coulomb blockade model, with a constant Coulomb interaction between any two electrons in the isolated region, was adequate. However, more careful analysis of data like that in figure 2 shows that this simple picture is inadequate. In particular, the difference in energies for electron spin parallel and antiparallel to the magnetic field are much too large in the experiment.

Recently, a density functional model for the isolated region has been developed in collaboration with Professor Lee’s group. The good agreement between the model and the experimental results shows that electrons in different spatial and even spin states have very different Coulomb interactions. This is a major step in deepening our understanding of the physics of single electron transistors.

**Publications**


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Figure 2. (a) The gate voltages corresponding to a series of consecutive conductance peaks like those in figure 1, as a function of magnetic field. (b) Result of subtracting a constant voltage from each successive peak in (a). This gives the energy for adding the next electron as a function of field, with the average Coulomb interaction subtracted. The arrows follow a particular level as it moves through successive peaks.

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Professor Patrick A. Lee