

The Circuit Abstraction

Circuits are important for two very different reasons:

- as physical systems
 - power (from generators and transformers to power lines)
 - electronics (from cell phones to computers)
- as models of complex systems
 - neurons
 - brain
 - cardiovascular system
 - hearing

The Circuit Abstraction

Circuits are the basis of our enormously successful semiconductor industry.





What is a Circuit?

Circuits are connections of components

- through which currents (through variables) flow and
- across which voltages (across variables) develop.



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Rule 2: Like the flow of water, the flow of electrical current (charged particles) is incompressible.

Example: flow of water through a branching point



What comes in must go out.

Here $i_1 = i_2 + i_3$.

Kirchoff's Current Law: the sum of the currents into a node is zero.

Rules Governing Flow

In electrical circuits, we represent current flow by arrows on lines representing connections (wires).



 $i_1 = i_2 + i_3.$

The dot represents a "node" which represents a connection of two or more segments.

Nodes

Nodes are represented in circuit diagrams by lines that connect circuit **components**.

The following circuit has three components, each represented with a box.



There are two nodes, each indicated by a dot. The net current into or out of each of these nodes is zero. Therefore $i_1 + i_2 + i_3 = 0$.

What is a Circuit?

Circuits are connections of components

- through which currents (through variables) flow and
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Rules Governing Voltages Voltages accumulate in loops.

Example: the series combination of two 1.5 V batteries supplies 3 V.





Kirchoff's Voltage Law: the sum of the voltages around a closed loop is zero.

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Rules Governing Components

Alternative Representation: Node Voltages

Node voltages represent the voltage between each node in a circuit and an arbitrarily selected ground.



Node voltages and component voltages are different but equivalent **representations** of voltage.

• component voltages represent the voltages across components.

• node voltages represent the voltages in a circuit.



Node-Voltage-and-Component-Current (NVCC) Method

Combining KCL, node voltages, and component equations leads to the NVCC method for solving circuits:

- Assign **node voltage variables** to every node except ground (whose voltage is arbitrarily taken as zero).
- Assign component current variables to every component in the circuit.
- Write one constitutive relation for each component in terms of the component current variable and the component voltage, which is the difference between the node voltages at its terminals.
- Express KCL at each node except ground in terms of the component currents.
- Solve the resulting equations.

Analyzing Simple Circuits

Analyzing simple circuits is straightforward.



The voltage source determines the voltage across the resistor, v = 1V, so the current through the resistor is i = v/R = 1/1 = 1A.



The current source determines the current through the resistor, i = 1A, so the voltage across the resistor is $v = iR = 1 \times 1 = 1$ V.



Common Patterns

There are a number of **common patterns** that facilitate design and analysis:

- series resistances
- parallel resistances
- voltage dividers
- current dividers

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Series Combinations

The series combination of two resistors is equivalent to a single resistor whose resistance is the sum of the two original resistances.





 $v = R_1 i + R_2 i$



 $R_s = R_1 + R_2$

The resistance of a series combination is always larger than either of the original resistances.

Parallel Combinations

The parallel combination of two resistors is equivalent to a single resistor whose conductance (1/resistance) is the sum of the two original conductances.



The resistance of a parallel combination is always smaller than either of the original resistances.









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Loading Loading did not occur in LTI systems. Example: adding H_2 has no effect on Y $X \rightarrow H_1 \rightarrow Y$ $X \rightarrow H_1 \rightarrow H_2 \rightarrow Z$ $Y = H_1 X$ regardless of H_2 . A: whi



Buffering

Effects of loading can be diminished or eliminated with a buffer.

An "ideal" buffer is an amplifier that

- senses the voltage at its input without drawing any current, and
- sets its output voltage equal to the measured input voltage.



We will discuss how to use op-amps to make buffers in next lecture.

Summary

Circuits represent systems as connections of components

- through which currents (through variables) flow and
- across which voltages (across variables) develop.

There are a number of **common patterns** that facilitate design and analysis:

- series resistances
- parallel resistances
- voltage dividers
- current dividers

Buffers eliminate loading and thereby simplify design and analysis.

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