Thermally enhanced blue light-emitting diode

The MIT Faculty has made this article openly available. Please share how this access benefits you. Your story matters.
Thermally enhanced blue light-emitting diode

Jin Xue, Yuji Zhao, Sang-Ho Oh, William F. Herrington, James S. Speck, Steven P. DenBaars, Shuji Nakamura, and Rajeev J. Ram

Citation: Applied Physics Letters 107, 121109 (2015); doi: 10.1063/1.4931365

View online: http://dx.doi.org/10.1063/1.4931365

View Table of Contents: http://scitation.aip.org/content/aip/journal/apl/107/12?ver=pdfcov

Published by the AIP Publishing

Articles you may be interested in

Performance enhancement of blue light-emitting diodes with a special designed AlGaN/GaN superlattice electron-blocking layer

Performance enhancement of blue light-emitting diodes with AlGaN barriers and a special designed electron-blocking layer
J. Appl. Phys. 110, 093104 (2011); 10.1063/1.3651393

Raman and emission characteristics of a-plane InGaN/GaN blue-green light emitting diodes on r-sapphire substrates
J. Appl. Phys. 109, 043103 (2011); 10.1063/1.3549160

Enhanced characteristics of blue InGaN/GaN light-emitting diodes by using selective activation to modulate the lateral current spreading length

Thermally stable and highly reflective AgAl alloy for enhancing light extraction efficiency in GaN light-emitting diodes
Thermally enhanced blue light-emitting diode

Jin Xue,1 Yuji Zhao,2 Sang-Ho Oh,2 William F. Herrington,1 James S. Speck,3 Steven P. DenBaars,2,3 Shuji Nakamura,2,3 and Rajeev J. Ram1,a)

1Department of Electrical Engineering and Computer Science, Massachusetts Institute of Technology, Cambridge, Massachusetts 02139, USA
2Department of Electrical and Computer Engineering, University of California, Santa Barbara, California 93106, USA
3Materials Department, University of California, Santa Barbara, California 93106, USA

(Received 22 June 2015; accepted 7 September 2015; published online 24 September 2015)

We investigate thermoelectric pumping in wide-bandgap GaN based light-emitting diodes (LEDs) to take advantage of high junction temperature rather than avoiding the problem of temperature-induced efficiency droop through external cooling. We experimentally demonstrate a thermally enhanced 450 nm GaN LED, in which nearly fourfold light output power is achieved at 615 K (compared to 295 K room temperature operation), with nearly no reduction in the wall-plug efficiency (i.e., electrical-optical energy conversion efficiency) at bias V < hω/q. The LED is shown to work in a mode similar to a thermodynamic heat engine operating with charged carriers pumped into the active region by a combination of electrical work and Peltier heat (phonons) drawn from the lattice. In this optimal operating regime at 615 K, the LED injection current (3.26 A/cm²) is of similar magnitude to the operating point of common high power GaN based LEDs (5–35 A/cm²). This result suggests the possibility of removing bulky heat sinks in current high power LED products thus realizing a significant cost reduction for solid-state lighting. © 2015 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4931365]

A major driver for the mass production and deployment of light emitting diodes (LEDs) has been the rise in maximum achievable wall-plug efficiency (WPE)—defined as the ratio of the output optical power and the input electrical power. The lifetime cost of an LED is dominated by the operating cost which is inversely proportional to the wall-plug efficiency.1 Intensive research into efficient visible light emitting diodes (LEDs) has been the rise in maximum achievable wall-plug efficiency (WPE)—defined as the ratio of the output optical power and the input electrical power. The lifetime cost of an LED is dominated by the operating cost which is inversely proportional to the wall-plug efficiency.1 Intensive research into efficient visible light emitting diodes (LEDs) has been the rise in maximum achievable wall-plug efficiency (WPE)—defined as the ratio of the output optical power and the input electrical power. The lifetime cost of an LED is dominated by the operating cost which is inversely proportional to the wall-plug efficiency.1

The ratio V=q is the electrical-optical energy conversion efficiency for a single photon emission event, where hω is photon energy, q is unit charge, and V is bias voltage. The ensemble WPE (ΘWP) for an LED includes the additional factor ηEQE describing the photon emission probability

ηWP = hω/qV ηEQE. (1)

θEQE has the maximum value of unity and is commonly known as the external quantum efficiency (EQE) of LEDs. Here, it is defined to include electrical injection efficiency and light extraction efficiency, as well as IQE.

Conventionally, high output powers for III-Nitride LEDs are achieved under high forward bias qV > hω in a passively cooled ambient. However, these widely used nitride LEDs inevitably exhibit IQE degradation (thus EQE and WPE) at high current densities with the production of excessive heat. The increased junction temperature further reduces IQE and output power. These well-known current induced and temperature induced efficiency droop phenomena have hampered the further enhancement of III-Nitride high power LEDs and imposed additional cost on the heat sink.12,13 In recent years, the dominant mechanisms of current induced efficiency droop, including Auger recombination, carrier leakage, density activated defect recombination, etc., have been extensively studied,12,14–21 and remedies have been considered.21–27 However, a comprehensive study and solution to the temperature induced efficiency droop (or thermal droop) was not reported.

---

a)E-mail: rajeev@mit.edu
In this letter, we investigate the possibility of thermoelectric pumping in wide-bandgap GaN based LEDs to take advantage of high junction temperature rather than avoiding the problem of thermal droop through external cooling. We experimentally demonstrate a thermally enhanced 450 nm high power GaN LED, in which a nearly fourfold enhancement in light output power is achieved at 615 K (compared to 295 K room temperature operation), with virtually no reduction in the wall-plug efficiency. In this optimal operating regime at 615 K, the LED injection current (3.26 A/cm²) is of similar magnitude to the operating point of common high power GaN based LEDs (5–35 A/cm²). This result suggests the possibility of removing the bulky heat sinks in current commercial high power LED products, thus realizing significant cost reduction and improved design flexibility.

The LED’s enhanced high temperature performance is fundamentally attributed to thermoelectric pumping as the maximum WPE is observed at a bias $V < \hbar\omega/q$. In such a low bias regime, thermal excitation due to Peltier effect becomes necessary for a considerable amount of carrier injection. In other words, the LED works in a mode similar to a thermodynamic heat engine operating with charged carriers pumped into the active region by a combination of electrical work and Peltier heat (phonons) drawn from the lattice,28 as depicted in Figure 1(a). The absorption of Peltier heat near junction area can be better understood from the perspective of statistical mechanics: only carriers at the high energy tail of Boltzmann distribution can be thermionically emitted above the diode’s built-in potential barrier into the active region, with the remaining carriers absorbing heat from lattice (i.e., electron-phonon scattering) to restore the quasi-equilibrium distribution. If lattice temperature increases, carrier temperature also increases and thus the corresponding distribution profile moves towards higher energy. In this case, the thermionic emission can happen more easily, and the Peltier effect is more conspicuous. However, it is notable that, although the thermoelectric pumping mechanism introduces an additional pathway of input energy, the improvement of LED WPE at the low bias, high temperature regime is still conditional since fewer injected carriers result in radiative recombination. Therefore, to realize an effective thermal enhancement of the LED performance at the proposed operation regime, the maintenance of a relatively high device IQE is a necessary condition, which requires low thermal droop as well as low current droop.

According to the standard rate equation model, the internal quantum efficiency of an LED can be written as

$$\eta_{\text{IQE}} = \frac{BN^2}{AN + BN^2 + CN^3},$$  \hspace{1cm} (2)

where $N$ is the carrier density in the LED quantum well (QW) active region, and $A$, $B$, $C$ are the associated Shockley-Read-Hall (SRH), radiative, and Auger recombination coefficients, respectively. The $B$ and $C$ coefficients scale with the magnitude of the overlap of the electron and hole wavefunctions:29 Here, we investigate the optimization of the quantum efficiency at low-bias ($V < \hbar\omega/q$) condition in which the Auger recombination is not dominant. Hence, we focus on maximizing the radiative bimolecular recombination ($B$) while minimizing the non-radiative SRH recombination process. Both of these goals are achieved by growing the LED structure on a free-standing semi-polar GaN substrate.

For conventional GaN based heterostructure LEDs grown on c-plane sapphire substrates, the [0001] growth direction inherently results in a large discontinuity of piezoelectric polarization density in the GaN/InGaN/GaN QW active region. The polarization discontinuity then induces an internal electrical field $\varepsilon_{pz}$ across the QW region, which is anti-parallel but much larger than the build-in field $\varepsilon_{bi}$ of an LED junction. This strong net field causes the band structure to tilt in the QW region, displaces the electron and hole wavefunctions to opposite sides of the well, and leads to a significant reduction of wavefunction overlap.27 For the sample studied in this work, however, this effect is largely mitigated through engineering the strength of $\varepsilon_{pz}$, i.e., growing LED in semi-polar [0021] direction to reduce $\varepsilon_{pz}$ and make it partially cancel with $\varepsilon_{bi}$ under low-bias operating conditions. In this case, the waveform overlap is maximized in the flat QW region and hence the radiative

![Fig. 1](image-url)

**Fig. 1.** (a) Band diagram of the InGaN single-quantum-well LED studied, the mechanism of thermoelectric pumping is also illustrated. (b) Epitaxial structure of the same sample device.
recombination rate is high (large B coefficient). The corresponding band structure of the sample studied is depicted in Figure 1(a). Band tilting is exaggerated to illustrate the effect of field in typical GaN based LEDs. For conventional GaN LEDs grown on c-plane sapphire substrates, the defect (mainly threading dislocation) density is on the order of $10^8 \text{cm}^{-2}$, while this value can be as low as $10^5 \text{cm}^{-2}$ for those directly grown on free-standing GaN substrates. Therefore, the high-quality growth of the LED active region on a free-standing semi-polar GaN substrate offers the possibility of reducing defect associated SRH recombination rate, while the improved wavefunction overlap increases the radiative recombination rate.

In the present work, a 450 nm small area (0.1 mm$^2$) InGaN single-quantum-well LED specifically designed for high power and low current-induced efficiency droop is studied. The epitaxial structure of the sample device studied is shown in Figure 1(b). It consists of an n-type GaN layer (1 μm thick with Si doping concentration of $1 \times 10^{19} \text{cm}^{-3}$), followed by 10 pairs of In$_{0.01}$Ga$_{0.99}$N/GaN (3/3 nm) undoped buffer layers, a GaN/In$_{0.18}$Ga$_{0.82}$N/GaN single QW (10 nm GaN bottom barrier, 12 nm InGaN QW and 15 nm GaN upper barrier), an Al$_{0.18}$Ga$_{0.82}$N electron blocking layer (EBL, 3 nm with Mg doping concentration of $2 \times 10^{19} \text{cm}^{-3}$), and a p-type GaN layer (50 nm with Mg doping concentration of $4 \times 10^{19} \text{cm}^{-3}$). Through the approach of high-quality semi-polar growth on GaN substrate, this high power LED exhibits low current induced efficiency droop as expected. An encapsulated sample with backside roughening and a ZnO vertical-stand package has an EQE of 50.1% and a LOP of 140 mW at 100 A/cm$^2$. To facilitate experiments over a wide thermal range, the LEDs tested were bare dies without packaging or encapsulation, resulting in lower EQE and WPE measurement values than devices optimized for high light extraction.

A schematic of the experimental setup is shown in the inset of Figure 2. During the testing, an LED die (approximately 5 mm by mm) is placed on the plane surface of a hemispherical sapphire solid-immersion lens (10 mm in diameter) for probing. The hemisphere is antireflection coated sapphire to approximately match the index of GaN substrate and has relatively high thermal conductivity. The sapphire lens sits in an opening of a copper arm that can be heated. Multiple thermocouples are attached to the surface of the GaN substrate and has relatively high thermal conductivity. The sapphire lens and the copper arm to monitor the temperature. A compound parabolic concentrator is placed a few millimeters underneath the sapphire lens with a large area (613.0 mm$^2$), calibrated silicon photo detector in the bottom for maximum light collection. In our measurements, pulse-mode voltage sweeps were performed from 2 to 5 V under ambient temperatures of 295 K, 375 K, 455 K, 535 K, and 615 K, respectively. Data are collected after the system reaches thermal equilibrium.

The dependence of EQE and WPE on LED current density at different temperatures is shown in Figure 2. As expected, EQE exhibits a small efficiency droop at high current density. It is notable that, although the peak EQE reduces with increasing temperature, the peak WPE has no significant drop. In fact, since the collection efficiency of the experimental setup is optimized at room temperature, thermal expansion of the heating stage relative to the photodetector is likely responsible for the small roll-off in the measured WPE at higher temperatures. The dependence of EQE and WPE on LED bias at different temperatures is also shown in Figure 3. It is observed that with the increase of temperature, peak WPE point gradually moves towards lower bias regime with virtually no reduction in value. As discussed in Equation (1), this is mainly because of the reduction in bias voltage, and more importantly for the minimal drop in EQE for large thermal excitation. A closer comparison for the two extreme cases at 295 K and 615 K is shown in Figure 4, with their corresponding LOP plotted together. As labelled in the figure, the bias voltage 3.35 V corresponding to peak WPE at 295 K has moved to 2.5 V at 615 K, with nearly fourfold increase in light output power and only 0.42% reduction in WPE (not calibrated for thermal expansion). The operating conditions for peak WPE at different temperatures are listed in Table I. It clearly tracks the continuous change of peak WPE and the corresponding EQE, indicating a growing LOP and reduced forward bias at higher temperatures. Note that the value of WPE starts to exceed EQE at 535 K, indicating the onset of low bias operation ($V < h\nu/q$) and effective thermo-electric pumping. In addition, the injection current density in the case of 615 K (3.26 A/cm$^2$) is already close to the value of 5 A/cm$^2$, which is the operating point of common high power GaN based LEDs.

![Figure 2](image-url)  
**FIG. 2.** EQE and WPE versus LED current density at different temperatures. A schematic of the experimental setup is shown in the inset.

![Figure 3](image-url)  
**FIG. 3.** EQE and WPE versus LED bias voltage at different temperatures. The bias voltage corresponding to the photon energy is indicated.
This article is copyrighted as indicated in the article. Reuse of AIP content is subject to the terms at: http://scitation.aip.org/termsconditions. Downloaded to IP: 149.169.211.50 On: Thu, 24 Sep 2015 16:39:59

To further look into the role that Peltier effect plays at different operation conditions, the total LED heating power (per unit area) \( Q_{\text{total}} \) generated from all sources including Peltier heating (intraband phonon emission or absorption) \( Q_{\text{Peltier}} \), non-radiative carrier recombination \( Q_{\text{non-rad}} \), and Joule heating \( Q_{\text{Joule}} \) arising from series resistance can be analyzed and expressed as

\[
Q_{\text{total}} = Q_{\text{Peltier}} + Q_{\text{non-rad}} + Q_{\text{Joule}} = JV \times (1 - \eta_{\text{WPE}}),
\]

where \( J \) is the injection current density of LED. An approximate magnitude for the Peltier heating component \( Q_{\text{Peltier}} \) can be determined from

\[
Q_{\text{Peltier}} = \left( V - IR_s - \frac{\hbar \omega}{q} \right) \times J,
\]

where \( R_s \) is the total series resistance of LED. It can be observed that the total heating power \( Q_{\text{total}} \) is always a positive so long as the wall-plug efficiency is below 100%, while the total Peltier heating component could become negative at low bias regime \( V < \hbar \omega / q \), indicating a transition from Peltier heating to Peltier cooling. For instance, a plot of \( Q_{\text{total}} \) versus LED bias at 295 K and 615 K is shown in Figure 5. A clear comparison in the relative magnitude of Peltier cooling power for the two temperature cases shows its dependence on the junction temperature: the Peltier effect is greatly enhanced at larger thermal excitation (i.e., higher junction temperature), mainly due to the exponential increase of injection current, according to Equation (4). The Peltier heating power (per unit area) for peak WPE operation at different temperatures is also listed in Table I.

**TABLE I. Conditions of reaching peak WPE at different temperatures.**

<table>
<thead>
<tr>
<th>T (K)</th>
<th>V (V)</th>
<th>J (A/cm²)</th>
<th>( P_{\text{OUT at Max WPE}} ) (W/cm²)</th>
<th>( \eta_{\text{WPE}} ) (%)</th>
<th>( \eta_{\text{WP, MAX}} ) (%)</th>
<th>( Q_{\text{Peltier}} ) (W/cm²)</th>
</tr>
</thead>
<tbody>
<tr>
<td>295</td>
<td>3.35</td>
<td>0.61</td>
<td>2.40</td>
<td>14.31</td>
<td>11.77</td>
<td>3.46 × 10⁻¹</td>
</tr>
<tr>
<td>375</td>
<td>3.10</td>
<td>1.00</td>
<td>3.61</td>
<td>13.07</td>
<td>11.61</td>
<td>3.12 × 10⁻¹</td>
</tr>
<tr>
<td>455</td>
<td>2.95</td>
<td>2.11</td>
<td>7.01</td>
<td>12.06</td>
<td>11.26</td>
<td>3.43 × 10⁻¹</td>
</tr>
<tr>
<td>535</td>
<td>2.75</td>
<td>2.94</td>
<td>9.06</td>
<td>11.19</td>
<td>11.21</td>
<td>-9.83 × 10⁻²</td>
</tr>
<tr>
<td>615</td>
<td>2.50</td>
<td>3.26</td>
<td>9.24</td>
<td>10.30</td>
<td>11.35</td>
<td>-9.02 × 10⁻¹</td>
</tr>
</tbody>
</table>

Again the change of sign of these values indicates the initiation of effective thermoelectric pumping above 535 K.

This low bias optimal regime of high LOP and high WPE at elevated temperature does not universally exist for common GaN based LEDs. The demonstration of the sample studied is attributed to the low current droop and low thermal droop for the EQE in this device. To achieve low thermal droop, reducing the density of defect states in LEDs is crucial, as the trap assisted SRH non-radiative recombination dominates the temperature dependence at low-bias.\(^{30}\) By assuming that traps capture electrons and holes at the same rate, the SRH recombination lifetime has approximately the following temperature dependence:\(^{32}\)

\[
\tau_{\text{SRH}} = \tau_0 \left( 1 + \cosh \frac{E_{\text{Trap}} - E_{\text{Fi}}}{kT} \right),
\]

where \( \tau_0 \) is a constant dependent on defect density and carrier capture rates. Increasing the density of defect states results in smaller \( \tau_0 \). \( E_{\text{Trap}} \) and \( E_{\text{Fi}} \) are the trap energy level and intrinsic Fermi level within the energy bandgap, respectively. According to the temperature dependence of Equation (5), we can see that a higher temperature LED junction results in an exponentially shorter SRH lifetime and thus a lower IQE.

An empirical relation describing LED light output power at different temperatures (for a constant current density) is given by\(^{30}\)

\[
P_{\text{OUT}}(T) = P_{\text{OUT, 300K}} \times \exp \left( -\frac{T - 300 \text{ K}}{T_C} \right),
\]

where \( T_C \) is the device characteristic temperature, with higher value indicating better thermal droop performance. At a current density around 10 A/cm², compared to the \( T_C \) values of less than 200 K for typical GaN based LEDs grown on c-plane sapphire substrates,\(^{30}\) samples we studied here exhibit a much higher value of 869 K, indicating a lower decay rate in IQE with increasing temperature, which is likely due to the reduced defect density in the LED active region.
In summary, by taking advantage of thermoelectric pumping, we demonstrate a thermally enhanced 450 nm high power GaN LED, in which nearly fourfold LOP is achieved at 615 K (compared to 295 K room temperature operation), with nearly no reduction in the wall-plug efficiency. Under this 615 K maximum WPE operation condition at $V < h\nu/q$, the LED injection current (3.26 A/cm$^2$) is found to be close to the value of 5 A/cm$^2$, which is the operating point of common high power GaN based LEDs. This result suggests the possibility of removing bulky heat sinks in commercial high power LED products, bringing a considerable reduction in the cost for general illumination.

This work was supported by Professor Amar G. Bose Research Grants and fellowship from Agency for Science, Technology and Research (A*STAR), Singapore. The work at UCSB was supported by the Solid State Lighting and Energy Electronics Center (SSLEEC). The authors would like to thank Parthiban Santhanam for having worked on the initial thermodynamic calculations of white light LEDs that helped motivate this work. The authors also thank Professor Claude Weibusch for useful discussions.