Dynamic-Range Analysis and Maximization of Micropower $G_m-C$ Bandpass Filters by Adaptive Biasing

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Abstract—We analyze and present an input gain-varying scheme for maximizing dynamic range in a well-known $G_m-C$ bandpass filter by both minimizing noise for small input signals, and by achieving balanced swing levels at all filter nodes for large input signals. A micropower bandpass filter suitable for use in cochlear implants and other power-constrained biomedical applications was implemented and tested in subthreshold CMOS. At a center frequency of 1.4kHz and quality factor of 4, the filter has 70dB of dynamic range (57dB maximum SNR, 2.5% total harmonic distortion (THD)) and consumes 2.55W of power.

I. INTRODUCTION
Low-power, wide-dynamic-range bandpass filters tunable in the audio-frequency range are an important component in cochlear implant processors and other biomedical devices [1], [2]. The power consumption of $G_m-C$ filters increases with their dynamic range of operation, such that achieving a large dynamic-range of operation is expensive in power. $G_m-C$, as well as other types of filters, are often designed with a balanced internal signal swing condition [3], [4]. The balanced condition ensures that when the input is large and the output node operates at the maximum available swing, no other internal node attempts to exceed this maximum swing. This approach maximizes the largest signal that can be handled by the filter, it does not improve the filter’s noise performance. Other design approaches that seek to minimize noise [5] do not discuss how noise reduction affects the filter’s balanced-swing condition at the upper end of its dynamic range.

To address these issues, we consider the effects of bias current choices on the filter’s overall dynamic range, taking into account both noise and the maximum allowable signal. We find that different biasing strategies within the same filter topology are needed to achieve minimal noise and maximum signal swing. Since transconductors can be easily tuned electronically by varying a bias current, we suggest a dynamic electronic tuning strategy to automatically modify the biases of individual transconductors (OTAs) to achieve either minimal noise or maximal signal swing as the input amplitude changes, without altering the filter’s time constant or quality factor. Thus, by adaptively biasing the filter as the level of the input signal changes, gains in overall dynamic range may be realized.

II. FILTER ANALYSIS
A. Transfer Function Analysis
Fig. 1 shows a $G_m-C$ implementation of the bandpass filter described in this work. The topology itself is well known [6]. We will begin by analytically determining dynamic range of the filter in completely general terms, allowing the transconductances of each OTA to take on any value. We shall assume that white noise is dominant in all OTAs, which we later experimentally demonstrate to be a good approximation for evaluating dynamic range.

Fig. 2 shows a block diagram of the same filter, with noise sources included. First, we ignore the noise sources and compute the transfer function of the filter:

$$v_{out} = G_{m1} \frac{G_{m4}C_2}{G_{m2}G_{m3}s^2 + G_{m4}C_2}$$

(1)

Eqn. 1 can be rewritten in a more standard, canonical form, as follows:

$$\frac{v_{out}}{v_{in}} = A \cdot \frac{\tau s}{\tau^2 s^2 + \tau s + Q}$$

where expressions for $A$, $\tau$, and $Q$ are given below:

$$A = \frac{G_{m1}}{G_{m4}}$$

$$\tau = \sqrt{\frac{C_1C_2}{G_{m2}G_{m3}}}$$

$$Q = \sqrt{\frac{C_1 \sqrt{G_{m2}G_{m3}}}{C_2 G_{m4}}}$$

The transfer function from the input to the intermediate node, $v_1$, is given by:
\[
v_{11} - v_{\text{out}} = \frac{G_{m2}}{sC_2} = \frac{G_{m1}}{\tau^2s^2 + \frac{G_{m3}}{Q} + 1}
\]

(2)

B. Noise Analysis

To determine the effect of noise, we ignore the input, \( v_{\text{in}} \), and consider the output noise currents of each OTA. The noise source \( i_{\text{134}} \) in describing the filter’s noise and power performance:

C. Optimal Filter Parameter Selection

Let us define the following parameters which are useful for describing the filter’s noise and power performance:

\[
\frac{1}{\tau} = C/G_{m1},\quad G_{m2} = G_{m3} + G_{m4}
\]

The total integrated voltage squared output noise of the filter is found by integrating the current noise PSDs multiplied by the squared noise transfer functions to the output node obtained from the block diagram (Fig. 2). The integration procedure is described in [7].

\[
\frac{v_{\text{no}}^2}{\text{voltage}} = \frac{v_{\text{no}}^2}{\text{voltage}} = \frac{NqV_L}{4} \left( 1 + \frac{G_{m3} + G_{m4}}{C_1} + \frac{G_{m3}}{C_2} \right)
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(3)

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(3)

In this parameterized form, one can make several observations about how to lower the noise of the filter. Most obviously, one can try to reduce \( N \) (by changing the OTA topology, for example), or increase the value of \( C \). Lowering \( V_L \) will also reduce the noise power, but this will reduce the maximum signal power by \( V_L \). Therefore \( V_L \) should be increased as much as possible (ideally to \( V_{DD}/2 \)) to maximize dynamic range. We also observe that the input-referred noise does not depend on the center frequency, but on the output. Altering \( \beta \) may somewhat lower noise, but this advantage is negated by the excessive amounts of chip area required for \( \beta \neq 1 \). Increasing \( \alpha \) always lowers noise, especially if \( Q \) is large, but this strategy also increases power consumption. The same is also true for increasing

1 Capacitor area is proportional to \((\beta + 1/\beta)\). Hence, capacitor area is minimized when \( \beta = 1 \).
Capacitors $C_1$ and $C_2$ were 10pF each, setting $\beta = 1$. All OTA transconductances could be tuned by varying external bias current sources.

Single-ended and fully-differential versions of the same filter were built. The fully-differential version uses a similar OTA design as the single-ended version. A high-input-impedance common-mode feedback (CMFB) circuit using a wide-linear range differential-difference transconductor (DD-OTA) [10] and no passive components detects the common-mode output voltage of each differential OTA and feeds back an appropriate bias current to servo it to a fixed reference voltage. Fig. 4 shows one differential OTA with CMFB. The load capacitors are arranged such that each CMFB half-circuit sees a load capacitance of $C_A$ while each differential half-circuit sees $C = C_A + 2C_B$, where $C = \tau G_v$.

![Diagram of a high-level schematic of a fully-differential OTA with CMFB](image)

**Fig. 4.** High-level schematic of a fully-differential OTA with CMFB provided by a DD-OTA.

**IV. MEASUREMENTS**

**A. Single-Ended Filter**

A variety of measurements were made on the single-ended bandpass filter to experimentally evaluate the effect of varying $A$, $\alpha$, $G_v$, and $Q$ on the power, noise and dynamic range.

First, to establish programmability of the filter, parameters $A$, $f_0 = 1/(2\pi\tau)$, $Q$, and $\alpha$ were adjusted, strictly based on design, by varying the 4 OTA bias currents. Actual measured values of $Q$ and $f_0$ vs. their expected values are shown in Fig. 5. The center frequency rises more slowly with bias current than expected because the OTAs begin to operate above-threshold, where transconductance is no longer proportional to the bias current. Varying $A$ has almost no effect on $f_0$ or $Q$, as it should not because $G_{m1}$ controls only the gain. Varying $\alpha$ tended to cause variability in the measured $f_0$ and $Q$. In principle, the product of $G_{m2}$ and $G_{m3}$, on which $f_0$ and $Q$ in part depend, is constant for any value of $\alpha$. However, if above-threshold and on-chip current mirror mismatch effects are considered, the $G_{m2}G_{m3}$ product could vary with $\alpha$ and cause deviations from the expected values of $f_0$ and $Q$.

![Diagram of output noise PSD](image)

**Fig. 3.** Theoretical value of $\alpha$ which minimizes the product of the total input-referred noise and power consumption.

![Diagram of OTA DD-OTA](image)

Noise PSDs at the filter output were measured and integrated to find the total output noise power at a variety of settings of $A$, $f_0$, $Q$ and $\alpha$. Fig. 6 shows noise PSDs for 3 different values of $I_r$, the bias current which ultimately determines $G_v$, and by extension, $f_0$. The $Q$ setting was 4 and $\alpha$ and $A$ settings were 1. It is apparent that a $1/f$ noise contribution is present. As $I_r$ increases, the thermal noise floor is lowered, causing the $1/f$ corner frequency to increase, as seen in the data and reported in [9]. The $1/f$ noise is primarily due to $G_{m2}$ since $G_{m2}$’s noise transfer function to the output is lowpass, as can be determined from the block diagram in Fig. 2. Fortunately, the noise current contributions of $G_{m1}$, $G_{m3}$, and $G_{m4}$ are all bandpass to the output, making their $1/f$ noise contributions negligible.

![Diagram of output noise PSD](image)

**Fig. 5.** (left) Set vs. measured center frequency at $Q = 4$. (right) Set vs. measured $Q$ at $f_0 = 1.4$ kHz. Both plots are taken for various values of $A$ and $\alpha$.

![Diagram of output noise PSD](image)

**Fig. 6.** Output noise power spectral densities of the filter at multiple center frequencies.

![Diagram of output noise PSD](image)

Fig. 7 shows the aggregate results of many noise measurements, taken at a constant $f_0$ of 1.4kHz. In these plots, the integrated voltage noise shown has been input-referred, or divided by the filter gain $A$. Consider the left panel of Fig. 7. As the set value of $A$ was adjusted between 1 and 6 (the actual value of $A$ was close to the set value), the input-referred noise dropped as expected. At a particular value of

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A. increasing Q also increased noise, as expected. Finally, for any given values of A and Q, increasing α caused the noise to drop, all in accordance with theory. The most dramatic reductions in noise as α was increased were associated with high values of Q, also noted from theory. However, it is clear that far more significant wins in noise are possible by adjusting A rather than α, as α has little effect on the noise except when A = 1.

For example, suppose α = 1 and Q = 8. When A = 1, the input-referred noise is about 435V. When A = 6, this decreases to about 90V, or by approximately a factor of 4.8 = 13.7dB. Theory predicts the noise ratio should be about 5.3 or 14.5dB. The small discrepancy between theory and practice is likely caused by 1/f noise encountered in measurement but not included in the theory. Similar discrepancies exist for varying α, but it is clear that A is much more effective at reducing input-referred noise than α.

The right panel of Fig. 7 includes the effect of power consumption on the √(power × noise) figure-of-merit as A and α are increased. The plot shows that increasing A always offers better performance value than increasing α. However, if one desires to vary α, α = 2 offers optimal performance, similar to what was predicted by theory.

At \( f_0 = 1.4kHz \), \( Q = 4 \), \( \alpha = 1 \), the filter had 70dB of dynamic range. The noise floor was 90μV with \( A = 6 \) and the maximum signal amplitude was 255mV\(_{\text{in}}\) with \( A = 1 \). The upper end of the dynamic range was limited by THD, set at 2.5% maximum. With \( A = 1 \), the maximum SNR was 57dB. Power consumption ranged from 1.63 to 2.55μW as A increases from 1 to 6. To realize the full 70dB dynamic range, adaptation of A with changes in input level are necessary and were implemented manually. Future versions could incorporate an adaptive-biasing gain-control loop as in [7] to automate the process. A die microphotograph of the chip is shown in Fig. 8.

\[ V_{\text{n,input}} = A \cdot V_{\text{ref}} \]

\[ f = 1.4kHz \]

\[ \alpha \]

\[ Q = 8 \]

\[ A = 1 \]

\[ A = 2 \]

\[ A = 4 \]

\[ A = 6 \]

\[ \sqrt{\text{power}} \times \text{noise} \]

\[ \text{SNR} \]

\[ \text{THD} \]

\[ \text{dynamic range} \]

\[ \text{power consumption} \]

\[ \text{chip area} \]

\[ \text{measurement} \]

\[ \text{adaptation} \]

\[ \text{gain-control loop} \]

\[ \text{automation} \]

\[ \text{chip} \]

\[ \text{microphotograph} \]

\[ \text{filter} \]

\[ \text{dynamic range} \]

\[ \text{outlet} \]

\[ \text{fully-differential} \]

\[ \text{differential} \]

\[ \text{difference} \]

\[ \text{amplifier} \]

\[ \text{noise-canceling} \]

\[ \text{input-referred} \]

\[ \text{noise} \]

\[ \text{amplifier} \]

\[ \text{adaptation} \]

\[ \text{adaptation} \]

\[ \text{gain} \]

\[ \text{control} \]

\[ \text{loop} \]

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