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Fiber diode synthesis

Nicholas D. Orfa,b, Ofer Shapira, Fabien Sorin,b, Sylvain Danto,a, Marc A. Baldoc, John D. Joannopoulosd, and Yoel Finka,b,1

The synthesis of a high-melting temperature semiconductor in a low-temperature fiber drawing process is demonstrated, substantially expanding the set of materials that can be incorporated into fibers. Reagents in the solid state are arranged in proximate domains within a fiber preform. The preform is fluidized at elevated temperatures and drawn into fiber, reducing the lateral dimensions and bringing the domains into intimate contact to enable chemical reaction. A polymer preform containing a thin layer of selenium contacted by tin–zinc wires is drawn to yield electrically contacted crystalline ZnSe domains of sub-100-nm scales. The in situ synthesized compound semiconductor becomes the basis for an electronic heterostructure diode of arbitrary length in the fiber. The ability to synthesize materials within fibers while precisely controlling their geometry and electrical connectivity at submicron scales presents new opportunities for increasing the complexity and functionality of fiber structures.

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hermal fiber drawing is a process in which a macrostructured preform is heated and drawn into extended lengths of micro-structured fiber. New methods of increasing both the structural complexity of fibers and number of materials compatible with the drawing process are substantially expanding the functionality of photonic crystal and semiconductor device fibers (1–3). The drawing process, however, has until now been limited to materials that flow at the draw temperature. Approaches that attempt to circumvent this limitation include depositing materials inside (4–7) or onto the surface (8–11) of previously drawn fiber substrates. These methods do not take full advantage of the scaling associated with fiber drawing and are limited in their geometric complexity and the length over which uniform structures can be produced. Here we present a new method to synthesize in situ a chemical compound that has a melting temperature far exceeding the draw temperature. In this process, precursors are arranged in adjacent domains in a macroscopic preform. During thermal drawing, the fluid reactants come into contact and precipitate a new compound. To demonstrate the potential of this fiber draw synthesis method, a polymer fiber preform containing a thin selenium–sulfur layer next to eutectic tin–zinc wires is constructed and thermally coredrawn into a fiber consisting of electrically contacted crystalline ZnSe domains of sub-100-nm scales at the interface between the metallic domains and selenium layer. The formation of the interface compound is the basis for an electronic heterostructure and a thermally drawn fiber device exhibiting rectifying behavior. The ability to synthesize materials during the fiber draw process while maintaining precise and arbitrary geometries should lead to substantial advances in the form and function of fibers.

Results and Discussion

A fiber preform consisting of metallic Sn97Pb3 and Sn97Zn15 wires placed within an amorphous polymer cladding and spanned by a thin film of Se97S15 was constructed and drawn into an extended fiber (schematic drawings and SEM micrographs of the preform and fiber are shown in Fig. 1). The metal electrodes were placed in an alternating fashion such that each Sn97Pb3/

Se97S15/Sn97Zn15 combination forms an independently addressable electronic device. Neither the elemental selenium nor zinc reactants are liquid at the drawing temperature and thus incompatible with thermal drawing. However, the melting temperatures of both reactants can be depressed by alloying with either sulfur (in the case of selenium) or tin (for the zinc reactant). Upon exiting the drawing furnace, the selenium alloy is quenched into the amorphous state, but it easily devitrifies to the equilibrium crystalline phase when annealed for 1 h at 150°C (12) (see Methods for full experimental details). The distance between the metal electrodes in a single fiber device is approximately 15 μm, an order-of-magnitude smaller than previous composite device fibers, suggesting that significant device miniaturization and concomitant increases in device performance and density are possible in future fiber devices (13).

The fiber diode optoelectronic properties are detailed in Fig. 2. The current-voltage characteristics per unit fiber length as a function of voltage both in the dark and under illumination from an AM1.5G solar source is given in Fig. 2A. The Sn97Pb3 electrode is biased positive with respect to the Sn97Zn15 electrode in the forward direction (Fig. 2A, Inset). Rectifying behavior is clearly evident in the dark, the magnitude of which is limited by the device series resistance arising from the lateral photodiode geometry. A short-circuit current and open-circuit voltage (0.5 V) develops under illumination, demonstrating the existence of an internal electric field and suggesting future application as a distributed photovoltaic device. A plot of the inverse square capacitance versus applied bias reveals a built-in voltage of about 0.8 V (Fig. 2B). The sign of the applied bias in I-V and C-V measurements suggests that the Se97Sn73/Pb26 junction behaves ohmically and the Se97S15/Sn97Zn15 is rectifying. The spectral responsivity shown in Fig. 2C under reverse bias (~2 V) shows maximum response at 460 nm (corresponding to an external quantum efficiency of 2%) and a tail off at around 650 nm, consistent with a selenium bandgap of 1.9 eV. This fiber diode structure represents a major improvement over previous thermally drawn photoconductor fiber devices. In addition to the newly observed rectification, the noise-equivalent power (NEP) of this device is 1,000 times smaller than previous multimaterial thin-film photoconductor devices at 4.7 pW-Hz−1/2 (14).

The I-V and C-V characteristics suggest that the metal–semiconductor-metal fiber device is single sided, i.e., composed of one ohmic and one blocking barrier. This is a surprising result because of the large work function difference between the semiconductor (15) and metals (16) and the fact that selenium is known to readily form rectifying barriers when contacted to metals in planar devices (17). Indeed in our own control experiments, thermally deposited selenium devices readily formed rectifying contacts, but until now all fiber devices behaved ohmically.

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The authors declare no conflict of interest.

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This article contains supporting information online at www.pnas.org/lookup/suppl/doi:10.1073/pnas.1101160108/-/DCSupplemental.
(see SI Text). The disparity in the electrical behavior between the planar and fiber devices is due to the different processing methods. Whereas fiber devices experience long periods of time at elevated temperatures where molecular diffusivity is high during preform fabrication and fiber drawing, the thermal evaporation techniques used to fabricate the planar devices are essentially room temperature processes. As the range in work function and elemental composition of the control planar devices spans and includes that of the fiber devices, it is clear that the electronic behavior of fiber devices is not controlled by work function difference. In fact, Kelvin probe force microscopy (KPFM), energy dispersive spectroscopy (EDS) compositional analysis, and Raman spectroscopy provide direct evidence of the formation of a ZnSe compound at the interface between Se$_9$S$_4$ and Sn$_{85}$Zn$_{15}$, while it is this interface compound that distinguishes the rectifying junction from previous metal-semiconductor interfaces.

The spatial variation in surface potential was measured by KPFM (18) and compared with compositional measurements performed by EDS. Fig. 3 presents the KPFM measurements (i–iii) along with SEM-based EDS line scans of similar junctions (iv). KPFM measured topography (i) and work function maps (ii) are shown as well as representative line scans (iii). Changes in the height at the interfaces arise from differences in sputter rates of the metals and semiconductor (19). A sharp change in both topography and work function can be seen at the Sn$_{26}$Pb$_{26}$/Se$_9$S$_4$ metallurgical interface (Fig. 3A). The KPFM map and line scan shows the potential change occurs over a 400-nm region.

The combination of the EDS and KPFM data suggest that the electronic behavior at the metal–semiconductor interfaces in the drawn fiber devices is guided by diffusion or compound formation between the metal and semiconductor components. The Sn$_{26}$Pb$_{26}$/Se$_9$S$_4$ junction behaves ohmically despite the large potential drop observed on the Se$_9$S$_4$, and Sn$_{85}$Zn$_{15}$ interface by KPFM. EDS demonstrates that this interface is diffuse. Indeed, the formation of a diffuse interface is a classic method of creating ohmic contacts between metals and semiconductors (20). The Sn$_{26}$Pb$_{26}$/Se$_9$S$_4$ and Sn$_{85}$Zn$_{15}$ interface is more interesting. A potential gradient across the region in Fig. 3B between 1.1 and 2.5 μm is clearly visible, but the region does not have the same topography as the Se$_9$S$_4$ and Sn$_{85}$Zn$_{15}$ junction does not coincide with the selenium–sulfur regions rather than the tin-containing regions even though zinc was initially separate from the sulfur–selenium regions prior to drawing. Furthermore, several point scans show zinc and selenium appearing in a one-to-one ratio.

The coincidence of zinc and selenium in the EDS measurements suggest that the compound is likely ZnSe based. Indeed, rectifying behavior cannot be controlled by SnSe or SnSe$_2$ because the small potential barrier that would develop between selenium ($E_g = 1.9$ eV, ionization potential (IP) = 5.9 eV; refs. 17 and 21) and these compounds (SnSe$_2$, $E_g = 1.0$ eV, IP = 6.1 eV, ref. 22; SnSe, $E_g = 0.9$ eV, ref. 23, IP = 5.6 eV, ref. 24) cannot explain the built-in potential of selenium appearing in a one-to-one ratio.
0.8 V determined by both C–V and KPFM measurements. Furthermore, there were no signs of this compound formation at the SnSe/SnSePb26 interface even though SnSe and SnSe2 would be just as likely to form at either interface due to the equally high concentration of tin and selenium at both junctions. A large bandgap semiconductor such as zinc selenide \( (E_g = 2.7 \text{ eV}, \ IP = 6.8 \text{ eV}) \) (25) would, however, form a barrier to hole conduction and explain the rectifying behavior. Additional macroscopic X-ray diffraction measurements performed on zinc pieces incubated in molten selenium at the fiber drawing temperature (260 °C) confirm that crystalline ZnSe can form at the drawing temperature (see SI Text).

Scanning confocal Raman microscopy performed on a drawn fiber provide direct evidence of ZnSe compound formation inside the fiber. Raman images of a small SnSe particle that became mixed within the larger SnSe/Zn15 matrix during fiber drawing are shown in Fig. 4. This area highlights the existence of an interfacial compound completely encompassing the SnSeS particle. Fig. 4A–C shows intensity maps obtained by integrating the spectral ranges 200–215, 230–245, and 248–263 cm\(^{-1}\), corresponding to the three discrete peaks visible in the Raman spectra at 207, 238, and 252 cm\(^{-1}\), respectively. Fig. 4D shows individual Raman spectra at five locations within the 5 × 5-µm scan area. The high-intensity peak observed at location 1 (238 cm\(^{-1}\)) corresponds to the A1 bond-stretching mode of trigonal selenium, the most thermodynamically stable of selenium allotropes (26), whereas the peaks at 207 and 252 cm\(^{-1}\) clearly seen in the spectra taken at locations 2, 3, and 4 correspond to the transverse optical and longitudinal optical modes of ZnSe, respectively (27). Location 5 in Fig. 4 corresponds to the SnSe/Zn15 matrix, from which no Raman signal was observed. The broadband signal observed between approximately 160 and 550 cm\(^{-1}\) is due to semiconductor fluorescence. Several shoulders visible in the Raman spectra in Fig. 4D may imply the presence of ZnSe in SnSe and vice versa. Although these shoulders may indicate mixing of the selenium and ZnSe phases, they most likely appear because the semiconductor domains are on the order of the microscope resolution. Indeed, the presence of substantial SnSe within ZnSe would result in electrical short-circuiting not seen in electronic characterization. Taken together, Fig. 4 clearly shows the presence of an interfacial region between the SnSe/Zn15 matrix and SnSeS particle, and this interfacial region is composed primarily of ZnSe. This result is consistent with the KPFM measurements, which also found the existence of an interfacial compound between SnSe/Zn15 and SnSeS.

A preliminary band diagram of the proposed SnSeS/ZnSe/SnSePb26 heterostructure is shown in Fig. 5. The diagram is constructed by combining the observed change in local vacuum energy levels at the SnSe/Zn15 interface and the SnSeS/ZnSe interface with the interfacial compound formation. The presence of an interfacial SnSeS/ZnSe layer would result in a larger density of states and altered electronic properties compared to the bulk materials.

**Fig. 3.** Surface potential and chemical composition of SnSeS/ZnSePb26 (A) and SnSeS/ZnSe/Zn15 (B) junctions. KPFM-measured topography and work function maps are shown in i and ii, respectively. Maroon lines highlight the metallurgical interface between metal and semiconductor. Colored arrows correspond to location of line scans in iii. EDS line scans show variation in composition across the metallurgical junction in iv. (Scale bar: 1 µm.)
level (i.e., contact potential) by KPFM with band offsets and built-in voltage calculated by Anderson’s model of heterostructures given the material’s bandgap and electron affinity (20, 28). The band diagram clearly indicates how the large discontinuity in the valence band at the Se$_2$S$_3$/ZnS interface would create a barrier to hole flow, and the estimated valence band discontinuity between Se$_2$S$_3$ and ZnS of 0.7 eV is nearly equal to the observed built-in voltage of 0.8 eV. There is expected to be negligible difference between ZnSe and a statistical ZnS$_{15}$ alloy in either the Raman spectrum (29) or electronic bandgap (30), and the effect of local composition fluctuations would be minimal.

**Conclusions**

The ability to synthesize specific materials in specific locations within thermally drawn fibers represents a significant advance in fiber processing and suggests that many more materials can be built into composite fibers than previously thought. Zinc selenide, for example, has many interesting optical and electronic properties (31), but its high melting temperature (1,530 °C) previously precluded it from use in all types of thermal drawing. It is likely that other compound semiconductors may be incorporated into fibers with a similar method. One must simply identify materials that may be codrawn together that could react to form a new composition. The present work shows that metal chalcogenide semiconductors can be synthesized during fiber drawing. Given recent advances in the areas of silicon- and germanium-based fibers, it is likely that compound semiconductors used in conventional microelectronics may also be incorporated into photonic or electronic fiber devices. The fabrication of rectifying devices within drawn fibers by synthesis of an interface compound itself is an important development and is perhaps reminiscent of the first recognition that metal silicides can substantially improve the repeatability and reliability of metal–silicon contacts (32). This fiber photodiode exhibits a three-order-of-magnitude improvement in NEP over previous fiber photodiode devices (14). Furthermore, the ability to form both ohmic and rectifying junctions is important for creating a host of different, increasingly complex electronic circuits. The ability to create blocking junctions may also be useful in improving the performance of newly demonstrated fiber transistors (12), and this work should lay the groundwork for many advances in the function of fiber devices on both the individual and array level.

**Methods**

**Fabrication.** Se$_2$S$_3$ was synthesized from high-purity elements (Alfa Aesar) using the standard melt quenching technique. Elements in the correct proportion were inserted into a quartz ampoule under inert atmosphere and then transferred to a vacuum line for additional purification by sublimation of volatile oxides (approximately 2 h at 190 °C). The ampoule was then sealed and inserted into a custom rocking furnace where it was slowly heated to 500 °C and mechanically rocked overnight to ensure homogenization. The ampoule was then quenched in water and the glassy compound was removed.

Preforms were fabricated by first milling semicircular slots into the outer diameter of a polyethersulfone (PSU) polymer tube with a Bridgeport endmill. Slot spacing and orientation was kept constant with a digital indexer set to rotate the PSU tube at specific angles. High-purity wires of Sn$_{97}$Pb$_{26}$ and Sn$_{96}$Zn$_{15}$ at. % (Sn$_{97}$Pb$_{27}$ and Sn$_{97}$Zn$_{3}$ wt %) from Indium Corporation were cut in half lengthwise and tightly fitted into the milled slots. A thick film (approximately 30 μm) of semiconductor was thermally evaporated onto a PSU substrate and then wrapped around the preform core so that the semiconductor and metal electrodes were touching. Additional layers of PSU were then wrapped around the device to impart mechanical strength. The resulting preform was fused into a single solid structure by heating under vacuum at 230 °C for 1 h and then slowly cooled to room temperature. The completed preform (having dimensions 26 mm in diameter, 120 mm in length) was then taken to an optical draw tower where it was thermally drawn into approximately 35 m of continuous device fiber (nominal diameter approximately 1 mm) at 260 °C.

**Characterization.** Current-voltage measurements were performed with a Keithley 6517a electrometer. Capacitance measurements were performed with a Hewlett Packard 4284a LCR meter at 20 Hz and 50 mV swing voltage to include the effects of all potential long-lived trap states. Photocurrent was measured as a function of wavelength for monochromated light and normalized by the measured responsivity at 530 nm. The NEP at 530 nm was calculated from the measured responsivity and the photodiode noise (taken to be shot noise). Samples for imaging and surface analysis were prepared by ion polishing with a JEOL cross-section polisher. Amplitude modulated KPFM was performed with an Omicron ultrahigh vacuum variable temperature atomic force microscope (base pressure <5 × 10$^{-10}$ torr) and a Kelvin probe control unit using a nanosensors Pt-Ir tip (nominal resonance ~75 kHz). Kelvin signal was run at the first overtone of cantilever resonance (approximately 475 kHz) with an applied peak-to-peak voltage of 200 mV to minimize tip-induced band bending. The tip work function was calculated by determining the contact potential difference (CPD) between the tip and a clean polycrystalline gold surface, and the sample work function was calculated by the relation $\Phi_{tip} - \Phi_{sample} = \Delta \Phi$.

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