Stochastic testing simulator for integrated circuits and MEMS: Hierarchical and sparse techniques

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Stochastic Testing Simulator for Integrated Circuits and MEMS: Hierarchical and Sparse Techniques

Invited Paper

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Abstract—Process variations are a major concern in today’s chip design since they can significantly degrade chip performance. To predict such degradation, existing circuit and MEMS simulators rely on Monte Carlo algorithms, which are typically too slow. Therefore, novel fast stochastic simulators are highly desired. This paper first reviews our recently developed stochastic testing simulator that can achieve speedup factors of hundreds to thousands over Monte Carlo. Then, we develop a fast hierarchical stochastic spectral simulator to simulate a complex circuit or system consisting of several blocks. We further present a fast simulation approach based on anchored ANOVA (analysis of variance) for some design problems with many process variations. This approach can reduce the simulation cost and can identify which variation sources have strong impacts on the circuit’s performance. The simulation results of some circuit and MEMS examples are reported to show the effectiveness of our simulator.

I. INTRODUCTION

As the device size shrinks to the sub-micro and nano-meter scale, process variations have led to significant degradation of chip performance and yield \cite{1, 2}. Therefore, efficient stochastic simulators are highly desired to facilitate variation-aware chip design. Existing circuit and MEMS simulators use Monte Carlo \cite{3, 4} for stochastic simulation. Despite its ease of implementation, Monte Carlo requires a huge number of repeated simulations due to its slow convergence rate, very often leading to prohibitively long computation times.

Stochastic spectral methods \cite{5–9} are promising alternative techniques. In fact, they have shown significant speedup over Monte Carlo in many engineering fields. The key idea is to represent the stochastic solution as a linear combination of some basis functions such as polynomial chaos \cite{10} or generalized polynomial chaos \cite{11}, which then can be computed by stochastic Galerkin \cite{5} or stochastic collocation \cite{11–13} techniques. Such techniques have been successfully applied to simulate the uncertainties in VLSI interconnects \cite{14–17}, electromagnetic and microwave devices \cite{18–20}, nonlinear circuits \cite{21–24} and MEMS devices \cite{25–27}.

An efficient stochastic testing simulator has been proposed to simulate integrated circuits \cite{28–30}. This simulator is a hybrid version of the stochastic collocation and the stochastic Galerkin methods. Similar to stochastic Galerkin, stochastic testing sets up a coupled deterministic equation to directly compute the stochastic solution. However, the resulting coupled equation can be solved very efficiently with decoupling and adaptive time stepping inside the solver. This algorithm has been successfully integrated into a SPICE-type program to perform various (e.g., DC, AC, transient and periodic steady-state) simulation for integrated circuits with both Gaussian and non-Gaussian uncertainties. It can also be easily extended to simulate MEMS designs (c.f. Section II). In this paper we will present two recent advancements based on this formulation.

First, Section III will present a hierarchical uncertainty quantification method based on stochastic testing. Hierarchical simulators can be very useful for the statistical verification of a complex electronic system and for multi-domain chip design (such as MEMS-IC co-design). In this simulation flow, we first decompose a complex system into several blocks and use stochastic spectral methods to simulate each block. Then, each block is treated as a random parameter in the higher-level system, which can be again simulated efficiently using stochastic spectral methods. This approach can be hundreds of times faster than the hierarchical Monte Carlo method in \cite{31}.

Second, in Section IV we will present an approach to improve the efficiency of stochastic spectral methods when simulating circuits with many random parameters. It is known that spectral methods can be affected by the curse of dimensionality. In this paper, we utilize adaptive anchored ANOVA \cite{52–57} to reduce the simulation cost. This approach exploits the sparsity on-the-fly according to the variance of the computed terms in ANOVA decomposition, and it turns out to be suitable for many circuit problems due to the weak coupling among different variation sources. This algorithm can also be used for global sensitivity analysis that can determine which parameters contribute the most to the performance metric of interest.

The simulation results of some integrated circuits and MEMS/IC co-design cases are reported to show the effectiveness of the proposed algorithms.

II. STOCHASTIC TESTING SIMULATOR

In this section we summarize the algorithms and results of our recently developed fast stochastic testing circuit simulator. We refer the readers to \cite{28–30} for the technical details.
A. Overview of the Simulator

The overall flow of the stochastic testing simulator is shown in Fig. 1. The main procedures are summarized below.

1) Set Up Stochastic Circuit Equations: Given a circuit netlist, the device models and the specification of device-level uncertainties, one can use modified nodal analysis to obtain a stochastic differential algebraic equation:

\[
\frac{dq}{dt}(\hat{x}(t), \xi) + f(\hat{x}(t), \xi) = Bu(t)
\]

where \(\hat{u}(t)\) is the input signal; \(\hat{x} \in \mathbb{R}^n\) denotes nodal voltages and branch currents; \(q \in \mathbb{R}^n\) and \(f \in \mathbb{R}^n\) represent the charge/flux and current/voltage terms, respectively. Here \(\xi = [\xi_1, \ldots, \xi_d] \in \Omega\) (with \(\Omega \subseteq \mathbb{R}^d\)) represents \(d\) independent random variables describing device-level uncertainties. The joint probability density function of \(\xi\) is

\[
\rho(\xi) = \prod_{k=1}^{d} \rho_k(\xi_k),
\]

where \(\rho_k(\xi_k)\) is the marginal density of \(\xi_k \in \Omega_k \subseteq \mathbb{R}\).

2) Stochastic Testing Formulation: When \(\hat{x}(\xi, t)\) has a bounded 2nd-order moment, we can approximate it by a truncated generalized polynomial chaos expansion.

\[
\hat{x}(t, \xi) \approx \hat{x}(t, \xi) = \sum_{\alpha \in \mathcal{P}} \hat{x}_\alpha(\xi) H_\alpha(\xi)
\]

where \(\hat{x}_\alpha(t) \in \mathbb{R}^n\) denotes a coefficient indexed by vector \(\alpha = [\alpha_1, \ldots, \alpha_d] \in \mathbb{N}^d\), and the basis function \(H_\alpha(\xi)\) is an orthonormal multivariate polynomial with the highest order of \(\xi\), being \(\alpha_i\). In stochastic testing, the highest total degree of the polynomials is set as \(p\), leading to \(\mathcal{P} = \{ \alpha | \alpha_k \in \mathbb{N}, 0 \leq \alpha_1 + \cdots + \alpha_d \leq p \}\). Consequently, the total number of basis functions is

\[
K = \binom{p + d}{p} = \frac{(p + d)!}{p!d!}.
\]

Since all components of \(\xi\) are assumed mutually independent, the multivariate basis function can be constructed as

\[
H_\alpha(\xi) = \prod_{k=1}^{d} \phi_{\alpha_k}(\xi_k),
\]

where \(\phi_{\alpha_k}(\xi_k)\) is a degree-\(\alpha_k\) univariate polynomial of \(\xi_k\) satisfying the orthonormality condition

\[
\langle \phi_{\gamma}^k(\xi_k), \phi_{\nu}^k(\xi_k) \rangle = \int_{\Omega_k} \phi_{\gamma}^k(\xi_k) \phi_{\nu}^k(\xi_k) \rho_k(\xi_k) d\xi_k = \delta_{\gamma, \nu}
\]

where \(\delta_{\gamma, \nu}\) is a Delta function; integers \(\gamma\) and \(\nu\) denotes the degrees of \(\xi_k\) in \(\phi_{\gamma}^k(\xi_k)\) and \(\phi_{\nu}^k(\xi_k)\), respectively. Given \(\rho_k(\xi_k)\), one can utilize a three-term recurrence relation to construct such orthonormal univariate polynomials [39]. The univariate generalized polynomial chaos basis functions for Gaussian, Gamma, Beta and uniform distributions can be easily obtained by shifting and scaling existing Hermite, Laguerre, Jacobi and Legendre polynomials, respectively [6, 8]. Since for any integer \(k \in [1, K]\) there is a one-to-one correspondence between \(k\) and \(\alpha_k\), for simplicity we rewrite (3) as

\[
\hat{x}(t, \xi) \approx \hat{x}(t, \xi) = \sum_{k=1}^{K} \hat{x}_k(\xi) H_k(\xi).
\]

In order to find \(\hat{x}(t, \xi)\), we need to calculate the coefficient vectors \(\hat{x}_k(t)\)'s. In stochastic testing, \(\hat{x}(t, \xi)\) is substituted into (1) and then the resulting residual is forced to zero at \(K\) testing points \(\xi^1, \ldots, \xi^K\), giving the following coupled deterministic differential algebraic equation of size \(nK\)

\[
\frac{dq(\hat{x}(t))}{dt} + f(\hat{x}(t)) = Bu(t),
\]

where the state vector \(\hat{x}(t) = [\hat{x}_1(t); \cdots; \hat{x}_K(t)]\) collects all coefficient vectors in (7). This new differential equation can be easily set up by stacking the function values of (1) evaluated at each testing point [28, 50].

In stochastic testing, the testing points are selected as follows [28, 30]:

Step 1. For each \(\xi_k\), select \(p + 1\) Gauss quadrature points \(\xi_k^j\)'s and weights \(w_k^j\)'s [40–42] to evaluate an integral by

\[
\int_{\Omega_k} g(\xi_k) \rho_k(\xi_k) d\xi_k \approx \sum_{j=1}^{p+1} g(\xi_k^j) w_k^j
\]

which provides the exact solution when \(g(\xi_k)\) is a polynomial of degree \(\leq 2p+1\) [40]. The \(d\)-dimensional quadrature points and weights for \(\xi_k\) are then obtained by a tensor rule, leading to \((p + 1)^d\) samples in total.

Step 2. Define a matrix \(V \in \mathbb{R}^{K \times K}\), the \((j, k)\) element of which is \(H_k(\xi^j_k)\). Among the obtained \((p + 1)^d\) \(d\)-dimensional quadrature points, select the \(K\) points with the largest weights as the final testing points, subject to the the constraint that \(V\) is invertible and well conditioned.

3) Simulation Step: Instead of simulating (1) using a huge number of random samples, our simulator directly solves the deterministic equation (8) to obtain a generalized polynomial-chaos expansion for \(\hat{x}(t, \xi)\). In DC and AC analysis, we only
need to compute the static solution by Newton’s iterations. In transient analysis, numerical integration can be performed given an initial condition to obtain the statistical information (e.g., expectation and standard deviation) at each time point.

This simulator is very efficient due to several reasons [28]. First, it requires only a small number of samples to set up [5] when the parameter dimensionality is not high. Second, the linear equations inside Newton’s iterations can be decoupled although [5] is coupled, and thus the overall cost has only a linear dependence on the number of basis functions. Third, adaptive time stepping can further speed up the time-domain stochastic simulation.

B. Performance Summary

Extensive circuit simulation examples have been reported in [28], showing promising results for analog/RF and digital circuits with a small to medium number of random parameters. For those examples, the stochastic testing simulator has shown $10^2 \times$ to $10^3 \times$ speedup over Monte Carlo due to the fast convergence of generalized polynomial-chaos expansions. This circuit simulator is also significantly more efficient than the standard stochastic Galerkin [5] and stochastic collocation solvers [11]–[13], especially for time-domain simulation.

Stochastic periodic steady-state solvers have been developed on this platform and tested on both forced circuits (e.g., low-noise amplifier) and autonomous circuits (e.g., oscillators) [29]. As an example, we consider the Colpitts BJT oscillator in Fig. 2 the frequency of which is influenced by the Gaussian variation of $L_1$ and non-Gaussian variation of $C_1$. With a 3rd-order generalized polynomial-chaos expansion, our stochastic testing simulator is about $5 \times$ faster than the solver based on stochastic Galerkin [23]. Fig. 3 shows the histograms of the simulated period from our simulator and the Monte Carlo simulator based on stochastic testing, showing that our stochastic testing simulator is about $5 \times$ faster than the Monte Carlo simulator.

C. Extension to MEMS Simulation

The stochastic testing method can be easily extended to simulate MEMS designs. Considering uncertainties, we can describe a MEMS device by a 2nd-order differential equation

$$M \left( \ddot{\xi} (\xi, t), \dot{\xi} \right) \frac{d^2 \xi (\xi, t)}{dt^2} + D \left( \dot{\xi} (\xi, t), \dot{\xi} \right) \frac{d\xi (\xi, t)}{dt} + \tilde{f} \left( \xi (\xi, t), u(t), \dot{\xi} \right) = 0$$

where $\tilde{\xi} \in \mathbb{R}^n$ denotes displacements and rotations; $u(t)$ denotes the inputs such as voltage sources; $M$, $D \in \mathbb{R}^{n \times n}$ are the mass matrix and damping coefficient matrix, respectively; $\tilde{f}$ denotes the net forces from electrostatic and mechanical forces. This differential equation can be obtained by discretizing a partial differential equation or an integral equation [43], or by using the fast hybrid platform that combines finite-element/boundary-element models with analytical MEMS device models [44]–[46]. First representing $\tilde{\xi}(\xi, t)$ by a truncated generalized polynomial-chaos expansion and then forcing the residual of (10) to zero at a set of testing points, we can obtain a coupled deterministic 2nd-order differential equation. This new 2nd-order differential equation can be directly used for stochastic static and modal analysis. For transient analysis, we can convert this 2nd-order differential equation into a 1st-order one which has a similar form with (8), and thus the algorithms in [28]–[30] can be directly used.

III. HIERARCHICAL UNCERTAINTY QUANTIFICATION

This section presents a hierarchical non-Monte Carlo flow for simulating a stochastic system consisting of several blocks. Let us consider Fig. 4 which can be the abstraction of a complex electronic circuit or system (e.g., phase-lock loops) or a design with multi-domain devices (e.g., a chip with both transistors and MEMS). The output of each block (denoted by $y_i$) depends on a group of low-level random parameters $\xi_i \in \mathbb{R}^{d_i}$, and the output of the whole system $\tilde{h}$ is a function of all low-level random parameters. Stochastic analysis for the whole system is a challenging task due to the potentially large problem size and parameter dimensionality. In this paper we assume that $\hat{x}_i$’s are mutually independent.

A. The Key Idea

Instead of directly simulating the whole system using $\hat{\xi}_i$’s as the random sources, we propose to perform uncertainty quantification in a hierarchical way.

1) Step 1: We use our fast stochastic spectral simulator [28], [29] to extract a surrogate model for each block

$$y_i = f_i(\hat{\xi}_i), \text{ with } \hat{\xi}_i \in \mathbb{R}^{d_i}, i = 1, \cdots, q.$$  

(11)

With the surrogate models, $y_i$ can be evaluated very rapidly. Note that other techniques [13], [31], [43] can also be utilized to build surrogate models. For numerical stability, we define

$$\zeta_i = (y_i - a_i)/b_i = \hat{f}_i(\hat{\xi}_i)$$  

(12)
such that $\zeta_i$ has a zero mean and unit variance.

2) Step 2: By treating $\zeta_i$'s as the new random sources, we compute $\tilde{h}$ by solving the system-level equation

$$F(\tilde{h}, \zeta) = 0,$$

with $\zeta = [\zeta_1, \ldots, \zeta_q]$. (13)

Again, we use the stochastic testing algorithm [28–30] to solve efficiently this system-level stochastic problem. Stochastic Galerkin and stochastic collocation can be utilized as well. Note that (13) can be either an algebraic or a differential equation, depending on the specific problems.

B. Numerical Implementation

The main challenge of our hierarchical uncertainty quantification flow lies in Step 2. As shown in Section II, in order to employ stochastic testing, we need the univariate generalized polynomial basis functions and Gauss quadrature rule of $\zeta_i$, which are not readily available. Let $\rho(\zeta_i)$ be the probability density function of $\zeta_i$, then we first construct $p + 1$ orthogonal polynomials $\pi_j(\zeta_i)$ via [39]

$$\pi_{j+1}(\zeta_i) = (\zeta_i - \gamma_j) \pi_j(\zeta_i) - \kappa_j \pi_{j-1}(\zeta_i),$$

$$\pi_0(\zeta_i) = 1, \quad \pi_j(\zeta_i) = 0, \quad j = 0, \ldots, p - 1$$

with

$$\gamma_j = \frac{\int \pi_j^2(\zeta_i) \rho(\zeta_i) d\zeta_i}{\int \pi_j^2(\zeta_i) \rho(\zeta_i) d\zeta_i}, \quad \kappa_j = \frac{\int \pi_j^2(\zeta_i) \rho(\zeta_i) d\zeta_i}{\int \pi_j^2(\zeta_i) \rho(\zeta_i) d\zeta_i}.$$ (14)

and $\kappa_0 = 1$. Here $\pi_j(\zeta_i)$ is a degree-$j$ polynomial with leading coefficient 1. After that, the first $p + 1$ basis functions are obtained by normalization

$$\phi_j(\zeta_i) = \frac{\pi_j(\zeta_i)}{\sqrt{\kappa_0 \kappa_1 \cdots \kappa_j}}, \quad \text{for } j = 0, 1, \ldots, p.$$ (15)

In order to obtain the Gauss quadrature points and weights for $\zeta_i$, we first form a symmetric tridiagonal matrix $J \in \mathbb{R}^{(p+1) \times (p+1)}$ with $J_{j,j} = \gamma_j - 1$, $J_{j,j+1} = J_{j+1,j} = \sqrt{\pi_j}$ and other elements being zero. Let its eigenvalue decomposition be $J = U \Sigma U^T$, where $U$ is a unitary matrix, then the $j$-th quadrature point and weight are $\Sigma_{j,j}$ and $\sqrt{\pi_j}$, respectively [40].

From (14) it becomes obvious that both the basis functions and quadrature points/weights depend on the probability density function of $\zeta_i$. Unfortunately, unlike the bottom-level random parameters $\xi_i$'s that are well defined by process cards, the intermediate-level random parameter $\zeta_i$ does not have a given density function. Therefore, the iteration parameters $\gamma_j$ and $\kappa_j$ are not known. In our hierarchical stochastic simulator, this problem is solved as follows:

- • When $f_i(\zeta_i)$ is smooth enough and $\zeta_i$ is of low dimensionality, we compute the integrals in (14) in the parameter space of $\zeta_i$. In this case, the multi-dimensional quadrature rule of $\zeta_i$ is utilized to evaluate the integral.
- • When $f_i(\zeta_i)$ is non-smooth or $\zeta_i$ has a high dimensionality, we evaluate this surrogate model at a large number of Monte Carlo samples. After that, the density function of $\zeta_i$ can be fitted as a monotone piecewise polynomial or a monotone piecewise rational quadratic function [47]. The special form of the obtained density function allows us to analytically compute $\gamma_j$ and $\kappa_j$. For further details on this approach, we refer the readers to [47].

C. MEMS/IC Co-Design Example

As a demonstration, we consider the voltage-controlled oscillator in Fig. 5. This oscillator has two independent identical MEMS capacitors $C_m$, the 3-D schematic of which is shown in Fig. 6. Each MEMS capacitor is influenced by four Gaussian-type process and geometric parameters, and the transistor threshold voltage is also influenced by the Gaussian-type temperature variation. Therefore, this circuit has nine random parameters in total. Since it is inefficient to directly solve the coupled stochastic circuit and MEMS equations, our proposed hierarchical stochastic simulator is employed.

1) Surrogate Model Extraction: The stochastic testing algorithm has been implemented in the commercial MEMS simulator MEMS+ [9] to solve the stochastic MEMS equation [10]. A 3rd-order generalized polynomial-chaos expansion and 35 testing points are used to calculate the displacements, which then provide the capacitance as a surrogate model. Fig. 7 plots the density functions of the MEMS capacitor from our simulator and from Monte Carlo using 1000 samples. The results match perfectly, and our simulator is about 30× faster.

2) Higher-Level Simulation: The obtained MEMS capacitor models are normalized as done in [12] (and denoted as $\zeta_1$ and $\zeta_2$). A higher-level equation is constructed, which is the stochastic differential algebraic equation in (1) for
this example. The constructed basis functions and Gauss quadrature points/weights for \(\xi_1\) are plotted in Fig. 8. The stochastic-testing-based periodic steady-state solver \[29\] is utilized to solve this higher-level stochastic equation to provide 3rd-order generalized polynomial expansions for all branch currents, nodal voltages and the oscillation period. In Fig. 9 the computed oscillator period from our hierarchical stochastic spectral simulator is compared with that from the hierarchical Monte Carlo approach \[31\]. Our approach requires only 20 samples and less than 5000 simulation, whereas the method in \[31\] requires 5000 minute for the higher-level stochastic simulation, whereas the method in \[31\] requires 5000 minute for the higher-level stochastic simulation.

IV. ANOVA-BASED SPARSE TECHNIQUE

Some circuit and MEMS problems cannot be simulated in a hierarchical way. When such designs have a large number of random parameters, the performance of stochastic spectral methods can significantly degrade, since the number of basis function \(K\) is a polynomial function of \(d\). To mitigate the curse of dimensionality in high-dimensional problems, sparsity of the coefficients in generalized polynomial coefficients can be exploited. This section presents a simulation flow that exploits such sparsity using anchored ANOVA (analysis of variance).

A. ANOVA and Anchored ANOVA Decomposition

1) ANOVA: Let \(y = g(\vec{\xi})\) be a performance metric of interest smoothly dependent on the independent random parameters \(\vec{\xi}\). Given a sample of \(\vec{\xi}\), the corresponding output \(y\) can be obtained by calling a deterministic circuit or MEMS simulator. With ANOVA decomposition \[32\], \[35\], we have

\[
y = g(\vec{\xi}) = \sum_{s\subseteq I} g_s(\vec{\xi}_s),
\]

where \(s\) is a subset of the full index set \(I = \{1, 2, \ldots, d\}\). Let \(\bar{s}\) be the complementary set of \(s\) such that \(s \cup \bar{s} = I\) and \(s \cap \bar{s} = \emptyset\) and \(|s|\) be the number of elements in \(s\). When \(\bar{s} = \{i_1, \ldots, i_{|s|}\} \neq \emptyset\), we set \(\Omega_s = \Omega_{i_1} \otimes \cdots \otimes \Omega_{i_{|s|}}\), \(\vec{\xi}_s = \{\xi_{i_1}, \ldots, \xi_{i_{|s|}}\} \in \Omega_s\) and have the Lebesgue measure

\[
d\mu(\vec{\xi}_s) = \prod_{k \in \bar{s}} (\rho_k(\xi_k) \, d\xi_k).
\]

Then, \(g_s(\vec{\xi}_s)\) in ANOVA decomposition \[16\] is defined recursively by the following formula

\[
g_s(\vec{\xi}_s) = \begin{cases} 
\mathbb{E}(g(\vec{\xi})) = \int g(\vec{\xi}) \, d\mu(\vec{\xi}) = g_0, & \text{if } s = \emptyset \\
\hat{g}_s(\vec{\xi}_s) = \sum_{t \subseteq s} \hat{g}_t(\vec{\xi}_t), & \text{if } s \neq \emptyset.
\end{cases}
\]

Here \(\hat{g}_s(\vec{\xi}_s) = \int g(\vec{\xi}) \, d\mu(\vec{\xi}_s)\), and the integration is computed for all elements except those in \(\vec{\xi}_s\). From \[18\], we have the following intuitive results:

- \(g_0\) is a constant term;
- if \(s = \{j\}\), then \(\hat{g}_s(\vec{\xi}_s) = \hat{g}_{(j)}(\xi_j)\), \(g_s(\vec{\xi}_s) = g_{(j)}(\xi_j) = \hat{g}_{(j)}(\xi_j) - g_0\);
- if \(s = \{j, k\}\) and \(j < k\), then \(\hat{g}_s(\vec{\xi}_s) = \hat{g}_{(j,k)}(\xi_j, \xi_k)\) and \(g_s(\vec{\xi}_s) = \hat{g}_{(j,k)}(\xi_j, \xi_k) - \hat{g}_{(j)}(\xi_j) - g_{(k)}(\xi_k) - g_0\);
- both \(\hat{g}_s(\vec{\xi}_s)\) and \(g_s(\vec{\xi}_s)\) are \(|s|\)-variable functions, and the decomposition \[16\] has \(2^d\) terms in total.

Since all terms in the ANOVA decomposition are mutually orthogonal \[32\], \[35\], we have

\[
\text{Var}(g(\vec{\xi})) = \sum_{s \subseteq I} \text{Var}(g_s(\vec{\xi}_s))
\]
where Var(\(\bullet\)) denotes the variance over the whole parameter space \(\Omega\). What makes ANOVA practically useful is that for many engineering problems, \(g(\xi)\) is mainly influenced by the terms that depend only on a small number of variables, and thus it can be well approximated by a truncated ANOVA decomposition

\[
g(\tilde{\xi}) \approx \sum_{|s| \leq m} g_s(\tilde{\xi}_s), \ s \subseteq \mathcal{I} \tag{20}
\]

where \(m \ll d\) is called the effective dimension. Unfortunately, it is still difficult to obtain the truncated ANOVA decomposition due to the high-dimensional integrals in (18).

2) Anchored ANOVA: In order to avoid the excessive multidimensional integrals, [35] has proposed an efficient algorithm which is called anchored ANOVA in [36], [37]. Assuming that \(\xi_k\)'s have standard uniform distributions, anchored ANOVA first chooses a deterministic point called anchored point \(\tilde{q} = [q_1, \cdots, q_d] \in [0, 1]^d\), and then replaces the Lebesgue measure with the Dirac measure.

\[
d\mu(\xi_k) = \prod_{k \in \mathcal{I}} (\delta(\xi_k - q_k) \, d\xi_k). \tag{21}
\]

As a result, \(g_0 = g(\tilde{q})\), and

\[
g_s(\tilde{\xi}_s) = g(\tilde{\xi}_s), \text{ with } \tilde{\xi}_k = \begin{cases} q_k, & \text{if } k \in \mathcal{s} \\ \xi_k, & \text{otherwise. } \end{cases} \tag{22}
\]

Anchored ANOVA was further extended to Gaussian random parameters in [36]. In [33], [37], this algorithm was combined with stochastic collocation to efficiently solve high-dimensional stochastic partial differential equations, where the index \(s\) was selected adaptively.

B. Anchored ANOVA for Stochastic Circuit Problems

In many circuit and MEMS problems, the process variations can be non-uniform and non-Gaussian. We show that anchored ANOVA can be applied to such more general cases.

Observation: The anchored ANOVA in [35] can be applied if \(p_k(\xi_k) > 0\) for any \(\xi_k \in \Omega_k\).

Proof: Let \(u_k\) denote the cumulative density function for \(\xi_k\), then \(u_k\) can be treated as a random variable uniformly distributed on \([0, 1]\). Since \(p_k(\xi_k) > 0\) for any \(\xi_k \in \Omega_k\), there exists \(\xi_k = \lambda_k(u_k)\). Therefore, \(g(\xi_1, \cdots, \xi_d) = g(\lambda_1(u_1), \cdots, \lambda_d(u_d)) = \psi(\bar{u})\) with \(\bar{u} = [u_1, \cdots, u_d]\). Following (22), we have

\[
\tilde{\psi}_s(\bar{u}_s) = \psi(\bar{u}_s), \text{ with } \bar{u}_k = \begin{cases} p_k, & \text{if } k \in \bar{s} \\ u_k, & \text{otherwise, } \end{cases} \tag{23}
\]

where \(\bar{p} = [p_1, \cdots, p_d]\) is the anchor point for \(\bar{u}\). The above result can be rewritten as

\[
g_s(\tilde{\xi}_s) = g(\tilde{\xi}_s), \text{ with } \tilde{\xi}_k = \begin{cases} \lambda_k(q_k), & \text{if } k \in \bar{s} \\ \lambda_k(\xi_k), & \text{otherwise, } \end{cases} \tag{24}
\]

from which we can obtain \(g_s(\tilde{\xi}_s)\) defined in (18). Consequently, the decomposition for \(g(\tilde{\xi})\) can be obtained by using \(\tilde{q} = [\lambda_1(p_1), \cdots, \lambda_d(p_d)]\) as an anchor point of \(\tilde{\xi}\).

**Algorithm 1 Stochastic Testing Circuit Simulator Based on Anchored ANOVA.**

1: Initialize \(S_k\)'s and set \(\beta = 0\);
2: At the anchor point, run a deterministic SPICE simulation to obtain \(g_0\), and set \(y = g_0\);
3: for \(k = 1, \cdots, m\) do
4: for each \(s \in S_k\) do
5: run stochastic testing simulator to get the generalized polynomial-chaos expansion of \(\hat{g}_s(\tilde{\xi}_s)\);
6: get the generalized polynomial-chaos expansion of \(g_s(\tilde{\xi}_s)\) according to (18);
7: update \(\beta = \beta + \text{Var}(g_s(\tilde{\xi}_s))\);
8: update \(y = y + g_s(\tilde{\xi}_s)\);
9: end for
10: for each \(s \in S_k\) do
11: \(\theta_s = \text{Var}(g_s(\tilde{\xi}_s)) / \beta\);
12: if \(\theta_s < \sigma\) then
13: for any index set \(s' \in S_j\) with \(j > k\), remove \(s'\) from \(S_j\) if \(s \subset s'\);
14: end if
15: end for
16: end for

For a given effective dimension \(m \ll d\), let

\[
S_k = \{s | s \subset \mathcal{I}, |s| = k\}, \ k = 1, \cdots, m \tag{25}
\]

contain the initialized index sets for all \(k\)-variate terms in the ANOVA decomposition. Given an anchor point \(\tilde{q}\) and a threshold \(\sigma\), our adaptive ANOVA-based stochastic circuit simulation is summarized in Algorithm 1. The index set for each level is selected adaptively. As shown in Lines 10 to 15, if a term \(g_s(\tilde{\xi}_s)\) has a small variance, then any term whose index set includes \(s\) as a strict subset will be ignored. All univariate terms in ANOVA (i.e., \(|s| = 1\)) are kept. Let the final size of \(S_k\) be \(n_k\) and the total polynomial order in the stochastic testing simulator be \(p\), then the total number of samples used in Algorithm 1 is

\[
N = 1 + \sum_{k=1}^{m} n_k \frac{(k + p)!}{k!p!}. \tag{26}
\]

For most circuit problems, setting the effective dimension as 2 or 3 can achieve a high accuracy due to the weak couplings among different random parameters. For many cases, the univariate terms in ANOVA decomposition dominate the output of interest, leading to a near-linear complexity with respect to the parameter dimensionality \(d\).

C. Global Sensitivity Analysis

Algorithm 1 provides a sparse generalized polynomial-chaos expansion \(y = \sum_{|\tilde{q}| \leq p} y_\tilde{q} H_\tilde{q}(\tilde{\xi})\). From this result, we can identify how much each parameter contributes to the output by global sensitivity analysis. Two kinds of sensitivity information can
be used to measure the importance of parameter $\xi_k$: the main sensitivity $S_k$ and total sensitivity $T_k$, as computed below:

$$S_k = \sum_{\alpha_k \neq 0, \alpha_j = 0} |y_{\alpha_k}|^2 \frac{\text{Var}(y)}{\text{Var}(y)}, \quad T_k = \sum_{\alpha_k \neq 0} |y_{\alpha_k}|^2 \frac{\text{Var}(y)}{\text{Var}(y)}.$$  

(27)

**D. Circuit Simulation Example**

Consider the CMOS folded-cascode operational amplifier shown in Fig. 10. This circuit has 53 random parameters describing the device-level uncertainties (variations of temperature, threshold voltage, gate oxide thickness, channel length and width). We set $p=3$, $m=3$ and $\sigma=0.01$ for this example, aiming to extract a generalized polynomial chaos expansion for the static voltage of $V_{\text{out}}$ (other quality of interest such as DC gain and total harmonic distortion can also be extracted). Directly using stochastic testing requires 27720 samples, which is too expensive on a regular workstation. Using the ANOVA-based sparse simulator, only 90 terms are needed to achieve a similar accuracy with Monte Carlo using 5000 samples: besides the constant term, only 53 univariate terms and 36 bivariate terms are computed, and no 3-variable terms are required. Our simulator uses 573 samples and less than 1-min CPU time to obtain a sparse generalized polynomial-chaos expansion with only 267 non-zero coefficients. Note that the full truncated anchored ANOVA requires 24858 terms and 482513 samples, which costs $842 \times$ more than our simulator.

Fig. 11 shows the computed main sensitivity and total sensitivity resulting from all device-level random parameters. Clearly, the uncertainty of the output is dominated by only a few number of device-level variations. The indices of the five device-level variations that contribute most to the output variation are 1, 50, 51, 11 and 46.

**V. CONCLUSION**

This paper has demonstrated a fast stochastic circuit simulator for integrated circuits and MEMS. This simulator can provide $100 \times$ to $1000 \times$ speedup over Monte Carlo when the parameter dimensionality is not high. Based on this simulator, a hierarchical stochastic spectral simulation flow has been developed. This hierarchical simulator has been tested by an oscillator with MEMS capacitors, showing high accuracy and a promising $250 \times$ speedup over hierarchical Monte Carlo. For integrated circuits with high parameter dimensionality, a sparsity-aware simulator has been further developed based on anchored ANOVA. This simulator has an almost linear complexity when the couplings among different parameters are weak and a small number of parameters dominate the output of interest. This simulator has been successfully applied to extract the sparse generalized polynomial-chaos expansion of a CMOS amplifier with over 50 random parameters, at the cost of less than 1-minute CPU time. Based on the obtained results, global sensitivity has been analyzed to identify which parameters affect the output voltage the most.

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**REFERENCES**

