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# Efficient, Hysteresis-Free and Flexible Inverted Perovskite Solar Cells Using

# **All-Vacuum Processing**

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# Abstract

The fabrication of efficient perovskite solar cells (PSCs) using all vacuum processing is still challenging due to the limitations in vacuum-deposition of the hole transporting layer (HTL). In this study, we fabricated inverted PSCs by employing the Copper (II) Phthalocyanine (CuPC) as an ideal alternative HTL for vacuum-processing. After proper optimization, a PSC with a power conversion efficiency (PCE) of 20.3% was achieved, which is much better than the PCE (16.8%) of the devices with solution-based CuPC. Since it takes long time to dissolve the CuPC for the solution-based device, the evaporation approach has a better advantage in terms of fast processing. Additionally, the device with the evaporated CuPC HTL indicates an excellent operational stability by showing only 9% PCE loss under continuous illumination after 100 h, better than its counterpart device. Interestingly, our device shows negligible hysteresis. Since all fabrication processes were

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performed at low temperature, we also fabricated flexible PSCs on ITO/PET substrates and obtained a PCE of 18.68%. After 200 bending cycles, our flexible device retained 87.5% of its initial PCE value, indicating its great flexibility. This study highlights the role of a suitable HTL for the fabrication of all vacuum-processing PSCs with great efficiency and stability.

Keyword: Inverted design, Perovskite, CuPC, Solar cell, HTL, Vacuum deposition, efficiency

### Introduction

Perovskite materials with a crystal structure of ABX<sub>3</sub> (A: formamidinium (FA), methylammonium (MA), Cs; B: Pb Sn; X: I, Br, Cl) show amazing optoelectronic properties, suitable for the fabrication of photovoltaic (PV) devices.<sup>1-7</sup> Low-cost and low-temperature processing, high mobility, band gap tunability and high diffusion length of the perovskite films impressed many researchers in the PV field.<sup>8-15</sup> Over the last decade, the efficiency and stability of the perovskite solar cells (PSCs) have been improved significantly by employing several approaches such as interface engineering, compositional engineering and surface passivation techniques.<sup>16-22</sup> Generally, there are two methods for the fabrication of PSCs, solution processing and vacuum approach.<sup>23-25</sup> Using both techniques, researchers demonstrated PSCs with power conversion efficiency (PCE) of over 20%.<sup>26,27</sup> However, in terms of large-area devices and scale-up, vacuum techniques have more potential since the deposition of precursors can be controlled precisely much better than a solution method even without annealing the perovskite film.<sup>28,29</sup> There are several approaches under vacuum category, thermal evaporation in one-step<sup>30</sup> or two-step<sup>31</sup>, layer-by-layer deposition<sup>32,33</sup>, chemical vapor deposition (CVD)<sup>34</sup>, which can provide us high-quality perovskite films conformally deposited on any substrate with extremely large-area without any pinhole.<sup>35-38</sup>

Since a PSC is consisted of electrodes, electron transporting layer (ETL), hole transporting layer (HTL) and perovskite absorber layer, all the layers have to be deposited by a vacuum approach in order to fabricate an all-vacuum processed device. In this regard, finding a suitable HTL is the main challenge in order to achieve high efficiency PSCs. In this regard, NiO is an inorganic HTL showing a good potential

for the fabrication of high efficiency devices.<sup>39,40</sup> There are also many organic HTLs recently developed for efficient and stable PSCs,<sup>41,42</sup> but only small molecules HTLs have a great potential to be evaporated al.<sup>44</sup> decomposition.43 For thermally without anv instance. employed Hsiao et 4,4'-cyclohexylidenebis[N,N-bis(4-methylphenyl)benzenamine](TAPC) as an HTL with evaporation potential and reported all vacuum-processed PSCs with PCE of up to 17.6%. Tsai et al.45 proposed vacuum deposition of Tris[4-(5-phenylthiophene-2-yl)phenyl]amine (TPTPA) as a small molecule for HTL in the PSCs and achieved PCE of over 18%. Using interface engineering, this group further improved the PCE of their device up to 18.8%, which is the highest reported PCE in the literature for allvacuum processing PSCs.<sup>46</sup> Although there is a good progress in the field of all vacuum-based PSCs, PCE still is not high enough for the commercialization purpose. Therefore, more efforts in terms of better HTL and device architecture are required to further push the PCE over 20%, comparable with the stateof-art PSCs.

One of the great alternative HTLs for the fabrication of efficient PSCs is metal phthalocyanine, where the metal part could be zinc or copper. By employing copper phthalocyanine, Kim *et al.*<sup>47</sup> fabricated a stable PSC in a normal architecture and achieved a PCE of 18% using a solution-based CuPC. Interestingly, the metal phthalocyanine has a potential for the thermal evaporation. Therefore, fabrication of PSCs with all-vacuum processing is feasible using this type of HTLs. In this regard, Ioakeimidis *et al.*<sup>48</sup> employed zinc phthalocyanine (ZnPC) as an alternative HTL for the fabrication all-vacuum based PSCs and reported a PCE of 11.6%. Ke *et al.*<sup>49</sup> fabricated PSCs using all-vacuum processing by employing CuPC as an HTL in their conventional device architecture and reported a maximum PCE of 15.42%. In fact, they used FTO as an electrode (with high surface roughness), which is not a good choice for this purpose. Due to the high roughness of FTO glass, their ETL (C60) needs to be thicker and thus the parasitic absorption could be higher (lower current density). Therefore, finding suitable architecture by considering all technical issues and band alignment engineering could help to push the PCE even higher. In this study, we reported all-vacuum processed PSCs in an inverted architecture using CuPC as an HTL. We have fabricated PSCs in a vacuum chamber without breaking the vacuum. Here, we achieved an

inverted PSC with a high PCE of 20.3% and negligible hysteresis, which is much better than the PSCs consisted of a solution-based CuPC HTL with a PCE of only 16.8%. Furthermore, our device showed excellent operational stability. We also fabricated flexible PSCs on PET/ITO substrates and obtained PCE of 18.68% with good flexibility. This work indicates the significant role of HTL for the fabrication of all-vacuum processed and high efficiency PSCs.

# **Results and Discussion**

Figure S1 shows the thermogravimetric analysis (TGA) of the CuPC. From the result, CuPC has a great thermal stability up to 600 °C and it has a potential for the evaporation without decomposition. In fact, the degradation temperature with 5% weight loss ( $T_{d95}$ ) for the CuPC is around 537 °C. Figure 1a shows the UV-visible spectra of both spin-coated CuPC (s-CuPC) and evaporated CuPC (e-CuPC) films. In order to estimate their bandgaps, we considered the Tauc plots of the corresponding samples as shown in Figure 1b, indicating bandgaps of 1.7 eV for s-CuPC and 1.72 eV for e-CuPC. The absorption of s-CuPC is slightly higher than the e-CuPC due to its higher thickness. In order to investigate the role of e-CuPC in devices, we thermally evaporated methylammonium lead triiodide (MAPbI<sub>3</sub>) perovskite on CuPC HTLs using a layer-by-layer approach.<sup>33</sup> Figure S2 shows the absorbance of the perovskite film, indicating a bandgap of 1.55 eV. Figure 3c and 3d show the top-view scanning electron microscopy (SEM) images of the perovskite films are ~300 nm in both samples.

# AU.



**Figure 1.** (a) (b) UV-visible spectra of the CuPC films deposited by both evaporation and spincoating methods. Top-view SEM images of the perovskite films deposited on (c) s-CuPC and (d) e-CuPC films.

Photoluminescence (PL) spectroscopy of the perovskite films without and with CuPC HTLs are shown in Figure 2a. As seen, there is a strong quenching effect in PL signal of the e-CuPC sample compared with the pure perovskite film, suggesting a better charge transfer in this sample. For the case of s-CuPC sample, the quenching effect is slightly weaker. Time-resolved PL (TRPL) spectroscopy of the corresponding films shows also the same trend (Figure 2b). The fitting parameters of the TRPL curves are shown in Table S1. From the results, the e-CuPC sample has the shortest lifetime, which indicates a better dynamic charge transfer in this sample.



**Figure 2**. (a) Photoluminescence and (b) time-resolved photoluminescence spectra of the perovskite film without and with CuPC HTLs. (c) UPS measurement of the e-CuPC and s-CuPC films. (d) Band diagram of the PSC device with respect to both CuPC HTLs.

In order to investigate the role of e-CuPC in the inverted PSC design, we measured ultraviolet photoelectron spectroscopy (UPS) for the CuPC films, as shown in Figure 2c. From the results, the valence bands (VBs) of the e-CuPC and s-CuPC films were estimated to be -5.2 eV and -5.15 eV, respectively. In fact, the difference in valence band could be ascribed to the orientation and quality of the CuPC films, deposited with different techniques.<sup>19</sup> Figure 2d depicts the band diagram of the corresponding PSCs.<sup>50,51</sup> As seen, the band offset between the e-CuPC and perovskite films is 230 meV, which is slightly lower than that of (280 meV) the s-CuPC sample. Therefore, the hole transfer at the perovskite/e-CuPC interface can facilitate as also confirmed by TRPL measurement.

We also evaluated the photovoltaic (PV) properties of the PSCs fabricated on the corresponding HTLs in an inverted design. For this purpose, all the layers were thermally evaporated to complete a device architecture shown in Figures 3a and 3b. From the SEM and schematic images, the inverted PSC is consisted of ITO/CuPC/perovskite/C60/BCP/Ag. Figure 3c shows the current density (J)voltage (V) curves of the inverted PSCs fabricated on e-CuPC and s-CuPC HTLs measured under standard condition and reverse scan. Table 1 summarizes the PV parameters of these devices. The s-CuPC based device indicates an open circuit voltage (V<sub>OC</sub>) of 1.09 V, a short circuit current density (J<sub>SC</sub>) of 21.41 mA/cm<sup>2</sup>, a fill factor (FF) of 72% and a PCE of 16.8 %. In contrast, the e-CuPC based device shows much higher PV parameters (V<sub>OC</sub> of 1.108 V, J<sub>SC</sub> of 23.34 mA/cm<sup>2</sup>, and FF of 78.5%), resulting in a high PCE of 20.3%, which is among the best reported value in the literature for all-vacuum processed PSCs. The inset graph in Figure 3c illustrates the maximum power point tracking (MPPT) of these devices, indicating stabilized PCEs of 16.55% and 20.17% for the s-CuPC and e-CuPC HTLs based PSCs, respectively. Figure S3 shows the average PV parameters of the corresponding PSCs, which confirm the same trend as the best performing devices. Moreover, the J<sub>SC</sub> of the devices was evaluated by external quantum efficiency (EQE) measurement as shown in Figure 3d. The extracted J<sub>SC</sub> for the s-CuPC and e-CuPC based devices were 21.02 and 22.95 mA/cm<sup>2</sup>, respectively, which are well-matched with the J-V results (Table 1). Notably, one of the reasons behind lower J<sub>SC</sub> and FF in the s-CuPC device could be higher parasitic absorption and sheet resistance of the s-CuPC film, respectively, due to its higher thickness. Another possible reason can be correlated to the orientation of CuPC molecules after deposition. In fact, the orientation of CuPC film deposited by evaporation could be different from the solution processing and depending on the arrangement of the molecules, the absorbance and electrical properties of the film can be changed.<sup>19</sup> The difference in VBs of both CuPC films can be the result of difference film orientation.

To achieve the best performing devices, we optimized the thickness of CuPC HTLs for both solution and evaporation approaches (Figure S4). The optimum thicknesses of the CuPC for the

evaporation and spin-coating approaches were 7 nm and 20 nm, respectively. In fact, deposition of a uniform, thin and compact CuPC film using spin-coating method is not possible, while using evaporation technique, we can easily control the thickness of CuPC layer without getting any pinholes, which is a great advantage of the developed vacuum approach.

Stability is the most significant challenge in the field of PSC devices.<sup>52</sup> Figures 3e and 3f demonstrate the shelf-life and operational stability results of the investigated devices, respectively. The e-CuPC based device shows only 7% PCE loss after maintaining the device in ambient condition (~40% relative humidity (RH)) for 30 days), which is slightly more stable than the device fabricated on s-CuPC HTL (10%). Regarding the operational stability, the e-CuPC based device still shows better stability under continuous illumination for 100 h (9% PCE loss). This is most likely due to the better and smoother interface between the e-CuPC and perovskite films. Beside stability, we also evaluated the role of scan direction on the PV results. Figure S5 shows the average value of hysteresis indices (HIs) for the corresponding devices, calculated using the following formula:

HI (%) = 
$$((PCE_{backward} - PCE_{forward})/PCE_{forward}) \times 100$$

The average HI value for the e-CuPC based devices is 1.2%, which is slightly smaller than that of s-CuPC ones (1.5%), possibly due to the better perovskite/HTL interface.



**Figure 3**. (a) Cross-sectional SEM image of the PSC with e-CuPC HTL. (b) Schematic of the corresponding device. (c) J-V curves (the inset graph shows the MPPT results) and (d) EQE spectra of the PSCs fabricated on s-CuPC and e-CuPC HTLs. (e) Shelf-life stability and (f) operational stability measurements of the PSCs fabricated on both s-CuPC and e-CuPC HTLs.

To demonstrate the potential of all-vacuum processed PSCs using CuPC HTLs, we also fabricated flexible PSCs with the same device architecture on PET/ITO substrates. Figure 4a illustrates a photograph of a flexible PSC with PET/ITO/CuPC/perovskite/C60/BCP/Ag structure. The J-V This article is protected by copyright. All rights reserved

curve (Figure 4b) of the flexible device fabricated on the e-CuPC shows a  $V_{OC}$  of 1.1 V, a J<sub>SC</sub> of 22.65 mA/cm<sup>2</sup>, a FF of 75% and a PCE of 18.68%. The inset graph in Figure 4b indicates the MPPT measurement of this device with a stabilized PCE of 18.51%. The extracted J<sub>SC</sub> from the EQE measurement (Figure 4c) was calculated to be 22.12 mA/cm<sup>2</sup>, which is in good agreement with the J-V result (Table 1). The average PV parameters of the flexible PSCs based on e-CuPC HTL are shown in Figure S6. In fact, the average PCE value of the flexible PSCs fabricated on the e-CuPC HTL is ~17.8%, which is among the best reported value in the literature.<sup>50</sup> As seen in Figure S7, we also fabricated flexible device on s-CuPC HTL, however the device showed poor efficiency of 12.92% due to the difficulty in the fabrication of flexible PSCs using solution-based technique. This result highlights the advantage of vacuum approach for the fabrication of flexible PSCs.

In order to evaluate the mechanical property of our flexible device, we performed a bending test with a bending radius of 3 mm for 200 bending cycles (Figure 4d). We find that the e-CuPC based device maintains 87.5% of its initial PCE value after 200 cycles, which indicates a good flexibility as compared to the flexible PSCs reported in the literature.<sup>53</sup>

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**Figure 4**. (a) Photograph of the flexible PSC with e-CuPC HTL. (b) J-V curve (the inset graph is the MPPT of the device) and (c) EQE spectrum of the e-CUPC based PSC fabricated on PET/ITO substrate. (d) Bending test result of the PSC fabricated on PET/ITO substrate using e-CuPC HTL.



Table 1. Figure of merits for the PSCs devices fabricated on s-CuPC and e-CuPC HTLs under reverse scan.

Device	$V_{oc}(V)$	$J_{sc} (mA/cm^2)$	FF (%)	PCE (%)	J <sub>sc</sub> from EQE	MPP (%)
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CuPC-solution	1.09	21.41	72	16.8	21.02	16.55
CuPC-evaporation	1.108	23.34	78.5	20.3	22.95	20.17
Flexible device	1.1	22.65	75	18.68	22.12	18.51

#### Conclusions

In summary, we found that the metal phthalocyanine-based HTL is a great choice for the fabrication of efficient all-vacuum-processed PSCs. By employing CuPC HTL, we fabricated all the layers in the PSCs using thermal evaporation in an inverted design without breaking the vacuum in the chamber. As compared to the device consisted of a solution-based CuPC with a PCE of 16.8%, we obtained an all-vacuum processed PSC with an impressive PCE of 20.3%, which also showed negligible hysteresis. Based on this procedure, we also fabricated an efficient flexible PSC with a PCE of 18.68% and great flexibility, indicating the potential of vacuum-deposited CuPC HTL for the flexible PSCs. Moreover, our e-CuPC based device shows great operational stability.

#### **Experimental section**

#### **Device Fabrication**

ITO glasses and PET/ITO were cleaned in different bathes using ultrasonic for 20 min (Triton X-100 diluted in deionized (DI) water (3 vol%), DI water, acetone and ethanol). Before any deposition, the substrates were treated by oxygen plasma for 5 min to have a better surface for the deposition. Afterward, the substrates were placed inside the thermal evaporation chamber and first the CuPC was deposited with different thicknesses as an HTL (deposition rate: 0.2 Å/s). For the solution based CuPC film, first a solution of CuPC in chlorobenzene (10 mg/mL) was prepared by adding 4  $\mu$ L 4-*tert*-butyl pyridine (tBP) and 7.5  $\mu$ L lithium bis(trifluoromethanesulfonyl)imide (Li-TFSI, 170 mg/mL in acetonitrile). The CuPC solution was spin-coated at 3000 rpm for 40s. For other thicknesses, the spinning rate was changed. After deposition of CuPC HTLs, MAPbI<sub>3</sub> perovskite film was thermally evaporated in the same

chamber with a layer-by-layer approach, as previously reported in the literature.<sup>32,33</sup> The perovskite film was deposited in 10 steps by sequentially deposition of PbI<sub>2</sub> and MAI with deposition rates of 0.5 nm s<sup>-1</sup> and 1 nm s<sup>-1</sup>, respectively. During the evaporation, the vacuum level was  $4 \times 10^{-6}$  mbar. All depositions were performed in one chamber without breaking the vacuum. All thicknesses were estimated by a Alpha-Step 200 (Tencor). After deposition of perovskite, the film was annealed at 100 °C for 10 min. Then, a layer of C60 with 23 nm-thick was thermally evaporated on perovskite films as an electron transport layer (ETL). Finally, 8 nm-thick of BCP and 100 nm-thick of Ag were thermally evaporated as back contact to finalize the fabrication process.

#### Film characterization

Perovskite morphology and the cross-section of the devices were studied by a focused ion beam (FIB) SEM (Helios). The absorbance of CuPC and perovskite films were measured by a Varian Cary 5. PL measurement was performed by a Fluorolog 322 Horiba Jobin Ybon Ltd. For the TRPL measurement a picosecond pulsed diode laser (EPL-405) was used and during the measurement, the pulse width and excitation wavelength were 49 ps and 405 nm, respectively. The TRPL curves were fitted by a biexponential equation (I(t) =  $a_i \exp(-t/\tau_i)$ ;  $\tau_i$  and  $a_i$  are the lifetime and amplitude of each component, respectively). For the UPS measurement, a He I (21.2 eV) photon source was used and the measurement was performed using an AXIS NOVA (Kratos Analytical Ltd, UK).

UNIX/TGA7 (PerkinElmer) was used to record thermogravimetric analysis (TGA). During the measurement, both heating and cooling rates were 10 °C/min.

#### Device measurement

The PV results of PSCs were evaluated using a 2400 Series source meter (Keithley, USA) and a solar simulator (a xenon lamp with 450 W (Oriel, USA))). The J-V measurement was performed under standard AM1.5G condition with a light intensity of 100 mW cm<sup>-2</sup>. A shadow mask was used to fix the device area to 0.054 cm<sup>2</sup>. The voltage scan rate was to 10 mV/s in these measurements. For the EQE test, an Oriel QE-PV-SI (Newport Corporation) equipped with a constant white light bias (5 mW cm<sup>-2</sup>) was

used. For the shelf-life stability measurement, the devices kept in ambient condition under dark (~40% RH) over 30 days and measured over time. For the operational stability test, the devices kept under continuous illumination (LED lamp with 1 sun illumination) inside a nitrogen-filled glovebox for 100 h and measured over the time. The bending test was performed in ambient condition and with a bending radius of 3 mm.

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Here, we report all-vacuum processed perovskite solar cells in an inverted device architecture by using CuPC as an HTL and achieve PCEs of 20.3% and 18.68% on rigid and flexible substrates, respectively.

