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# Electrically-switchable foundryprocessed phase change photonic devices

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### Electrically-switchable foundry-processed phase change photonic devices

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#### ABSTRACT

Optical phase change materials (PCMs) are a unique class of materials which exhibit extraordinarily large optical property change (e.g. refractive index change  $> 1$ ) when undergoing a solid-state phase transition, and they have witnessed increasing adoption in active integrated photonics and metasurface devices in recent years. Here we report integration of chalcogenide phase change materials in the Lincoln Laboratory 8-inch Si foundry process and the demonstration of electrothermally switched phase-change photonic devices building on a wafer-scale silicon-on-insulator heater platform.

Keywords: Phase change materials, reconfigurable photonics, integrated photonics, metasurfaces

#### 1.OPTICAL PHASE CHANGE MATERIALS FOR ACTIVE PHOTONICS

Chalcogenide phase change materials<sup>1</sup> have been exploited for a plethora of emerging optical applications including optical switching<sup>2–11</sup>, photonic memory<sup>12–14</sup>, optical computing<sup>15–18</sup>, active metamaterial/metasurface<sup>19–30</sup>, reflective display<sup>31–33</sup>, and thermal camouflage $34-36$ . Electrical pulsing is often a preferred option for switching these PCM devices: unlike furnace annealing, it is capable of reversible switching of PCMs; and compared to laser switching, it can be readily integrated with nanophotonic devices to enable chip-scale reconfigurable optical platforms. Electrical switching via heaters made of metal<sup>37,38</sup>, ITO<sup>4,6,39</sup>, graphene<sup>40,41</sup>, and doped Si<sup>42–44</sup> have been demonstrated.

Despite the major strides on PCM-based photonic devices and electrical switching, integration of PCMs into the standard photonics foundry process represents an essential technical milestone for PCMs. Foundry process integration not only is the practical route toward scalable manufacturing of PCM devices, but also eases access to PCM components for the entire photonics community. Notably, PCMs are readily poised for CMOS backend integration with their non-epitaxial nature and low processing temperatures, evidenced by their seamless integration in the 3D XPoint memory architecture. We foresee that achieving the milestone will significantly expedite PCMs' integration into large switching matrices and open up emerging applications such as arbitrary wavefront synthesis, energy-efficient optical switching and routing, quantum optical networks, as well as scalable neuromorphic computing.

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Here we report integration of chalcogenide phase change materials in the Lincoln Laboratory 8-inch Si foundry process and the demonstration of electrothermally switched phase-change photonic devices building on a wafer-scale silicon-oninsulator (SOI) heater platform. We also significantly advance the state-of-the-art by demonstrating integration of lowloss PCMs,  $Ge_2Sb_2Se_4Te_1(GSST)^{45,46}$  and  $Sb_2Se_3^{47-49}$ , with the foundry-processed SOI heaters.



#### 2. DEVICE FABRICATION

Figure 1. Schematic fabrication process flow for the PCM-based integrated photonic devices

The schematic fabrication process flow for the PCM-based integrated photonic devices is illustrated in Fig. 1. 220 nm SOI wafers were fabricated with varying n-doping concentrations using the 90-nm CMOS line in Lincoln Laboratory's 200 mm wafer foundry. The doping of silicon was carried out using ion implantation of phosphorous with varying dose and implantation energy. The n<sup>++</sup> region was formed with a dose of  $10^{16}$  cm<sup>2</sup> and an ion energy of 80 keV. The n region of  $\sim$ 4  $\times$  10<sup>18</sup> cm<sup>-3</sup> was formed with a dose of 10<sup>14</sup> cm<sup>-2</sup> and an ion energy of 80 keV. The contact was fabricated with a Ti/TiN barrier following a metallization with aluminum and passivation with  $SiO<sub>2</sub>$ . 500 nm-wide SOI waveguides and PCM cells were fabricated following two electron beam lithography fabrication steps on an Elionix ELS-F125 system. The waveguides were patterned using ZEP 520A positive photoresist followed by chlorine etching of half the silicon thickness. A lift-off process with polymethyl methacrylate (PMMA) resist was used to open the windows for the subsequent thermal evaporation of PCM. The film deposition was performed using thermal evaporation from PCM bulk materials following previously established protocols. Bulk starting PCMs were synthesized using a standard melt-quench technique from highpurity (99.999%) raw elements<sup>50</sup>. The samples were annealed in an argon environment before depositing 15 nm of Al<sub>2</sub>O<sub>3</sub> by atomic layer deposition to obtain a conformal protective layer. The metasurface devices were fabricated using a similar process, with the only differences being that: 1) in step 14 in Fig. 1, the entire heater is blanket etched to a target thickness of 80 nm; and 2) the wafer backside was mechanically polished to enable metasurface devices operating in a transmissive mode. The devices were subsequently wire bonded (using 0.8 mil 99% Al-1% Si wires) and mounted onto a customdesigned PCB, which allowed for reproducible electrical contact (as compared to using contact probes).

Two low-loss PCMs were explored in this work. For metasurface applications, GSST offer enhanced index contrast. This is critical to enabling full  $2\pi$  phase coverage while maintaining an ultra-thin profile amenable to electrothermal switching<sup>51</sup>. For reconfigurable integrated photonic applications at 1550 nm,  $Sb_2Se_3$  was adopted to realize phase-only modulation, since both of amorphous and crystalline states of  $Sb_2Se_3$  offers vanishingly small losses<sup>47</sup>.

#### 3.PHASE-ONLY WAVEGUIDE MODULATORS

The quest for efficient programmable photonic circuits requires active optical components with low insertion loss, small form factor, and low electrical power consumption<sup>52–54</sup>. The most common low-loss phase shifter – the key building block – employs electrically driven heaters to exploit thermo-optic effects. However, these devices require a constant power supply, suffer from thermal crosstalk, and their sizes are typically >100 µm. Here, we demonstrate a novel alternative that meets all the aforementioned requirements. We use SOI waveguides with patterned  $n^{++}$  -  $n - n^{++}$  doped-silicon microheaters to electro-thermally switch, with a single pulse, a cell of  $Sb_2Se_3$  between its two optically distinct, nonvolatile states: amorphous and crystalline. Sb<sub>2</sub>Se<sub>3</sub> is an emerging PCM at the telecommunications wavelengths while offers a ∆n ≈ 0.77 upon switching between states with near-zero losses. This unique set of properties allows for phase-only modulation, which sets a precedent in a growing PCM photonics field familiar with PCMs for amplitude modulation at the telecom band.



Figure 2. (a) Colored SEM image of a π/2 phase shifter – the dotted lines marked the doped region. b) Simulated optical modes for Sb<sub>2</sub>Se<sub>3</sub> in both states. c) Reversible and continuous switching in a ring resonator with 3  $\mu$ m-long Sb<sub>2</sub>Se<sub>3</sub>. d) Continuous tuning of an unbalanced MZI. e) MZI switch with the four end-point states of the two 6  $\mu$ m-long Sb<sub>2</sub>Se<sub>3</sub> cells. f) Reversible switching of the top phase shifter in an MZI switch with a total of 125 complete cycles shown.

We used the device shown in Fig. 2a. The heater is built using phosphorus implantation with concentrations  $n \sim 4 \times 10^{18}$ cm<sup>-3</sup> and  $n^{++} \sim 10^{20}$  cm<sup>-3</sup>. We patterned a 500 nm waveguide on half-etched 220 nm SOI and thermally evaporated and annealed  $Sb_2Se_3$  with 30 nm thickness. The length of  $Sb_2Se_3$  is defined based on the target phase shift. From mode simulations (Fig. 2b), we found a  $\Delta n_{\text{eff}} \approx 0.071$ , which leads to a phase shift of 0.09  $\pi/\mu$ m. The non-negligible extinction coefficient result from the doped-silicon losses, which theoretically leads to  $0.011$  dB/ $\mu$ m. Experimentally, the total insertion loss was 0.03 dB/ $\mu$ m due to other effects such as scattering. To reversibly switch, we used 3.2 V × 100  $\mu$ s or 3.2  $V \times 1$  ms pulses to either crystallize partially or fully, and single 16 V  $\times$  1 µs or 21 V  $\times$  400 ns to amorphize. We demonstrate reversible and continuous (multi-state) switching of a 3  $\mu$ m-long Sb<sub>2</sub>Se<sub>3</sub> in Fig. 2c, which displays a phase shift of  $\pi/4$ , in excellent agreement with the theoretical value. We show a 1.64 $\pi$  phase shift in an MZI in Fig. 2d. We further demonstrated a  $2 \times 2$  MZI switch – key in programmable architectures – in which light can be switched between two outputs by applying  $\pi/2$  phase shift in both arms. We achieve this by using an ultra-compact 10  $\mu$ m-long heater with 6 µm-long  $Sb_2Se_3$  (Fig. 2e). Lastly, we show device cyclability over 250 switching events in Fig. 2f. Our results pave the way to zero static power consumption programmable photonics for applications in computing, quantum, telecom, and others.

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#### 4.ELECTRICALLY SWITCHABLE PCM METASURFACES



Figure 3. Simulated optical spectra of the PCM metasurface in (left) amorphous and (right) crystalline states



Figure 4. Measured reflectance spectra of the PCM metasurface

The metasurface consists of a GSST pillar array with a lateral size of 360 nm and a 780 nm square pitch size. Unlike devices reported by others where the PCM layer is limited to an ultrathin (< 100 nm) form factor to facilitate rapid quenching during re-amorphization and hence switching reversibility, the GSST layer in our device has a total thickness of 230 nm. Our previous work has proved that GSST exhibits improved amorphous phase stability compared to the classical GeSbTe alloys, which enables a much larger PCM thickness while maintaining fully reversible switching capability. The increased PCM volume affords substantially enhanced light confinement and interaction with the PCM layer. The simulated optical spectra of the metasurface is presented in Fig. 3. The metasurface is designed to exhibit large optical contrasts at the telecom wave band. A Thermo Fisher FTIR6700 Fourier transform infrared spectrometer with an attached microscope was used to obtain the reflectance spectra of the devices. The reflectance spectra were calibrated using a standard gold mirror. The corresponding measured reflectance spectra upon switching can be seen in Fig. 4. The change of 10 dB at 1430 nm shows the potential of such device as an active filter or amplitude modulator if intermediate crystallization states can be reliably reached. A Renishaw Invia Reflex micro-Raman system was used for collecting Raman spectra on the devices to validate that the observed optical contrast results from phase transformation of the PCM. The Raman spectra in Fig. 5 confirms reversible electrothermal switching between amorphous and crystalline states over multiple cycles.



Figure 5. Measured Raman spectra of the PCM metasurface confirming reversible switching

#### 5. SUMMARY

In the scope of this work, we demonstrated electrothermally switched PCM metasurface and waveguide devices, both fabricated leveraging standard Si photonic foundry processes. These results enable a new path for scalable manufacturing of PCM photonic devices and electrically driven infrared light control for both free-space and integrated photonics applications, such as tunable imaging optics, spatial light modulators, tunable spectral filters, subwavelength phased arrays, beam steering, and dynamic holography.

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