



$$AB = 2^3 b_3 A + 2^2 b_2 A + 2 b_1 A + b_0 A$$



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This report, No. 127 in a series of Progress Reports issued by the Research Laboratory of Electronics, contains the customary annual statement of research objectives and summary of research for each group. The report covers the period January 1, 1984–December 31, 1984, and the source of support is indicated for each project. On the masthead of each section are listed the academic and research staff and the graduate students who participated in the work of the group during the year. The listing of personnel in the back of the book includes only members of the laboratory during 1984.

**Cover:** Data dependence graph for a parallel multiplier. The length of the multiplier operand is 4 bits. Allen, J., "Computer Architecture for Digital Signal Processing," *Proc. IEEE*, Vol. 73, No. 5, May 1985, pp. 852-873.



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